Electrical Engineering and Information Technology

Abschluss: Bachelor

Module Manual

Valid from: 2007

Hint: you are in ***Summer 2007*** and not in the term which is in planning!

Electrical Engineering 1 - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-2118@lists.hs-weingarten.de		
Type of Course	Lecture	**Long text**	
Number	2118	**Short text**	
Term	SS 2007	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 3 12.03.2007 - 25.03.2007 enrollment Hauptbelegungszeitraum 2 05.03.2007 - 18.03.2007 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Mo.	08:00 to 09:30	woch		Gebäude B - B 309						
	Tues.	08:00 to 09:30	woch		Gebäude B - B 309						

Functions:

Group [no name]: → application info

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering & Information Technology	1 - 1		5

Assign to Departments

Electrical Engineering and Information Technology

Contents

Comment

Zur Auffrischung des erforderlichen Basiswissens werden zunächst die aus der Schulphysik bekannten Grundlagen der Elektrotechnik wiederholt. Den Schwerpunkt der Veranstaltung bildet die Berechnung von beliebigen linearen Netzwerken, zunächst bei Gleichstrom, dann unter Anwendung der komplexen Rechnung bei sinusförmigem Wechselstrom. Zum Abschluss wird das Verhalten von ohmschen Widerständen, Induktivitäten und Kapazitäten bei beliebigen zeitabhängigen Spannungen und Strömen untersucht.
Lehrinhalte dieses Moduls sind: Grundbegriffe, Gleichstromkreise (Kirchhoffsche Sätze, lineare Ersatzzweipole), Anwendungen, Strom/Spannungsteiler, Netzwerkberechnungsverfahren, Wechselgrößen und ihre Darstellung, komplexe Rechnung und ihre Anwendung bei Wechselstromgrößen, Netzwerkberechnung bei Wechselstrom mit wichtigen Anwendungsbeispielen, Grundzweipole bei beliebigen zeitabhängigen Spannungen und Strömen, Drehstrom.

Literature

Führer, u.a.: Grundgebiete der Elektrotechnik, Carl Hanser Verlag
Ameling, W.: Grundlagen der Elektrotechnik, Vieweg
Moeller/Frohne u.a.: Grundlagen der Elektrotechnik, Teubner
Ose, Rainer: Elektrotechnik für Ingenieure, Fachbuchverlag Leipzig
Weißgerber, Wilfried: Elektrotechnik für Ingenieure 1, 2, Vieweg

Remarks

Berechnung einfacher Gleichstrom- und Wechselstrom-Netzwerke

Exams accredited

unmarked: --- .
marked: written examination, 90 minutes.
(Hint: will be examined with
Electrical Engineering 1
Electrical Engineering 2)

Information and Communication Engineering (Diploma)
unmarked: --- .
marked: written examination, 120 minutes.
(Hint: will be examined with
Electrical Engineering 1
Electrical Engineering 2)

Structure Tree

This lecture was found in SoSe 2007 1 times:

Vorlesungsverzeichnis
→ Elektrotechnik und Informationstechnik
→ Grundstudium - - - 1

Hint: you are in ***Summer 2007*** and not in the term which is in planning!

Elektrotechnik 1 Übungen - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Required subject (SPO) | Departments | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-3007@lists.hs-weingarten.de		
Type of Course	Practical Exercise	**Long text**	
Number	3007	**Short text**	
Term	SS 2007	**Hours per week in term**	2
Expected no. of participants		**Max. participants**	13
Frequency	Every Term	**Study Year**	
Hyperlink			
application periods	Hauptbelegungszeitraum 2 05.03.2007 - 18.03.2007 **enrollment** Hauptbelegungszeitraum 3 12.03.2007 - 25.03.2007 **enrollment**		

Dates/Times/Location Group: 1. Group E-Mailadresseⓘ: lsf-veranstaltung-3007-gruppe-1@lists.hs-weingarten.de

Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
Tues.	16:00 to 17:30	woch		Gebäude H - H239						

Functions:

Group 1. Group: → application info

Dates/Times/Location Group: 2. Group E-Mailadresseⓘ: lsf-veranstaltung-3007-gruppe-2@lists.hs-weingarten.de

Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
Tues.	11:45 to 13:15	woch		Gebäude C - C009						

Functions:

Group 2. Group: → application info

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering & Information Technology	1 - 1		

Assign to Departments

Faculty Electrical Engineering & Computer Science

Structure Tree

This lecture was found in SoSe 2007 1 times:

Vorlesungsverzeichnis
→ Elektrotechnik und Informationstechnik
→ Grundstudium - - - 1

Course Overview
Course Overview (modularisiert)
Search for Lectures
Time table of study programs
Structure of Curricula modules-LSF
Edit Lecture
Lectures today
Lectures today (with Search)
Hide menu

Hint: you are in ***Summer 2007*** and not in the term which is in planning!

Lineare Algebra - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Required subject (SPO) | Departments | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-1888@lists.hs-weingarten.de		
Type of Course	Lecture	**Long text**	
Number	1888	**Short text**	
Term	SS 2007	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
application periods	Hauptbelegungszeitraum 2 05.03.2007 - 18.03.2007 enrollment Hauptbelegungszeitraum 3 12.03.2007 - 25.03.2007 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Thurs.	08:00 to 09:30	woch		Gebäude C - C004						
	Thurs.	09:45 to 11:15	woch		Gebäude C - C004						

Functions:

Group [no name]: → application info

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Business Informatics & E-Business	1 - 1		5

Assign to Departments

Business Informatics and E-Business

Structure Tree

This lecture was found in SoSe 2007 1 times:

Vorlesungsverzeichnis
→ Wirtschaftsinformatik und eBusiness
→ Grundstudium - - - 1

Hint: you are in ***Summer 2007*** and not in the term which is in planning!

Lineare Algebra mit Übungen - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructor | Required subject (SPO) | Departments | Structure Tree

Basic Information

E-Mailadresse	lsf-veranstaltung-3000@lists.hs-weingarten.de		
Type of Course	Lecture	**Long text**	
Number	3000	**Short text**	
Term	SS 2007	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
application periods	Hauptbelegungszeitraum 2 05.03.2007 - 18.03.2007 enrollment Hauptbelegungszeitraum 3 12.03.2007 - 25.03.2007 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Wed.	09:45 to 11:15	woch		Gebäude K - K 104		Benz				
	Wed.	11:45 to 13:15	woch		Gebäude K - K 104						

Functions:

Group [no name]: → application info

Responsible Instructor

Responsible Instructor	Responsibilities	Activity
Benz, Elmar		

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering & Information Technology	1 - 1		5

Assign to Departments

Faculty Electrical Engineering & Computer Science

Structure Tree

This lecture was found in SoSe 2007 1 times:

Vorlesungsverzeichnis
→ Elektrotechnik und Informationstechnik
→ Grundstudium - - - 1

Hint: you are in ***Summer 2007*** and not in the term which is in planning!

Digital technology - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-1850@lists.hs-weingarten.de		
Type of Course	Lecture	**Long text**	
Number	1850	**Short text**	
Term	SS 2007	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 2 05.03.2007 - 18.03.2007 enrollment Hauptbelegungszeitraum 3 12.03.2007 - 25.03.2007 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Fr.	08:00 to 09:30	woch		Gebäude H - H239						
	Fr.	09:45 to 11:15	woch		Gebäude H - H239						

Functions:

Group [no name]: → application info

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering & Information Technology	1 - 1		5

Assign to Departments

Faculty Electrical Engineering & Computer Science

Contents

Comment

Einführung, Grundbegriffe der Digitaltechnik, Darstellung boolescher Funktionen, Normalformen boolescher Funktionsgleichungen, boolesche Algorithmen. Schaltnetze, Beschreibung von Schaltnetzen, Minimierung von Schaltnetzen, Entwurf von Schaltketten, Schaltnetze (kombinatorische Schaltungen), Beschreibung von Schaltnetzen, Minimierung von Schaltnetzen, Entwurf von Schaltketten. Asynchrone Schaltwerke, Beschreibung von asynchronen Schaltwerken, Realisierungen von asynchronen Schaltwerken, Feinstruktur asynchroner Schaltwerke. Synchrone Schaltwerke, Funktionsbeschreibung durch Zustandsübergangstabelle und Ausgangsverknüpfung, Realisierungen von synchronen Schaltwerken als Einregister-Automat, Aufbau von synchronen Schaltwerken aus Operations- und Steuerwerken. Digitale Systeme, Standardfamilien, programmierbare digitale Systeme (PLD), Entwurfswerkzeuge für programmierbare digitale Systeme (PLD-Compiler).

Literature

Borucki, Lorenz Digitaltechnik, Teubner,
Fricke, Klaus Digitaltechnik Vieweg,
Leonhardt, Erich Grundlagen der Digitaltechnik
Lichtenberger, Bernhard Praktische Digitaltechnik, Hüthig-Verlag
Pernards, Peter Digitaltechnik I und II, Hüthig-Verlag
Prochaska, E. Digitaltechnik für Ingenieure, Oldenbourg,
Schiffmann, Schmitz Technische Informatik
Urbanski, Klaus, Woitowitz, Roland Digitaltechnik, Springer

Remarks

Vermittlung grundlegender Kenntnisse hinsichtlich Konzipierung, Berechnung und Aufbau von digitalen Systemen.
Die Vorlesung Digitaltechnik vermittelt grundlegende Kenntnisse hinsichtlich Konzipierung, Berechnung und Aufbau von digitalen Systemen. Beginnend mit einer einführenden Darstellung der in der Digitaltechnik verwendeten Begriffe erfolgt die Entwicklung des im weiteren Verlauf verwendeten Regelwerkes. Es schließt sich die Darstellung der Funktionsweisen von kombinatorischen Schaltungen und von Schaltwerken an. Der Aufbau digitaler Schaltungen wird unter Verwendung von Bausteinen aus Standardfamilien und unter Ausnutzung von programmierbaren digitalen Bausteinen erläutert.

Access requirements

keine

Exams accredited

unmarked: --- .
marked: written examination, 90 minutes.

Structure Tree

This lecture was found in SoSe 2007 1 times:

Vorlesungsverzeichnis
→ Elektrotechnik und Informationstechnik
→ Grundstudium - - - 1

Hint: you are in *Summer 2007* and not in the term which is in planning!

Digital technology, Practical training - Single View

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Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructors | Required subject (SPO) | Contents | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-1808@lists.hs-weingarten.de		
Type of Course	Practical training	**Long text**	
Number	1808	**Short text**	
Term	SS 2007	**Hours per week in term**	2
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 3 12.03.2007 - 25.03.2007 enrollment Hauptbelegungszeitraum 2 05.03.2007 - 18.03.2007 enrollment		

Dates/Times/Location Group: 1. Group E-Mailadresse ⓘ: lsf-veranstaltung-1808-gruppe-1@lists.hs-weingarten.de

Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
Thurs.	09:45 to 13:15	woch	12.04.2007 to 24.05.2007	Gebäude H - H 216		Jobke,				20

Functions:

Group 1. Group: → application info

Responsible Instructors

Responsible Instructors	Responsibilities	Activity
Jobke, Stephan, Professor a. D., Dr.-Ing.	responsible	
. . , Dipl.Ing.(FH)	attendant	

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering & Information Technology	1 - 1		2

Contents

Comments
Die Versuche werden immer in 2er Teams durchgeführt. Es gibt nur einen Nachholtermin pro Student! Das Skript gibt es bereits zu Vorlesungsbeginn im H217 (bei . oder Brasser melden)!
!!!! Der Termin am 03.05. entfällt wegen der Exkursion !!!!

Comment
Basic circuits in TTL and CMOS
Design of combinatorial circuits
Design of sequential circuits without programmable logic devices (EPLD)
Design of sequential circuits with programmable logic devices (EPLD)
Design of a finite state machine
Design of an operation and control circuit

Remarks
This practical training consists of exercises about design methods for digital systems. After an exercise about standard devices, several exercises about the programming of EPLDs will follow. The students have to prepare the exercises at home which will be executed in the laboratory on circuit panels and on personal computers. Each group of students has to prepare a written report about the exercise.

Exams accredited
unmarked: practical work.
marked: --- .

Applied Computer Science (Diploma)
unmarked: laboratory work.
marked: --- .

Structure Tree

This lecture was found in SoSe 2007 1 times:

Vorlesungsverzeichnis
→ Elektrotechnik und Informationstechnik
→ Grundstudium - - - 1

Hint: you are in ***Summer 2007*** and not in the term which is in planning!

Digital Systems and Basics of Electronics - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-1438@lists.hs-weingarten.de		
Type of Course	Lecture	**Long text**	
Number	1438	**Short text**	
Term	SS 2007	**Hours per week in term**	2
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 2 05.03.2007 - 18.03.2007 enrollment Hauptbelegungszeitraum 3 12.03.2007 - 25.03.2007 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Mo.	08:00 to 09:30	woch		Gebäude L - L129						

Functions:

Group [no name]: → application info

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Applied Computer Science	1 - 1		2

Assign to Departments

Faculty Electrical Engineering & Computer Science

Contents

Comment

- Overview
- Simplification of combinatorial circuits
- Realization of circuits
- Circuit dimensioning
- Tasks of a circuit
- Circuit families
- Introduction to Lab Work
- Code circuits
- Memory Chips
- Circuits, Sequential circuits
- Examples of different circuits
- PLD-Programming

Remarks

- Basic terms of digital technology
- Practical design and construction of simple circuits with IC's and PLD's
- Combinatorial and sequential circuits
- Codes and code locks
- Principle construction of a processor

Exams accredited

unmarked: --- .
marked: written examination, 60 minutes.

Structure Tree

This lecture was found in SoSe 2007 1 times:

Vorlesungsverzeichnis
→ Angewandte Informatik
→ Grundstudium - - - 1

Hint: you are in ***Summer 2007*** and not in the term which is in planning!

Processor technology - Single View

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Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructor | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	lsf-veranstaltung-4020@lists.hs-weingarten.de		
Type of Course	Lecture	**Long text**	
Number	4020	**Short text**	RTECH
Term	SS 2007	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	120
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 3 12.03.2007 - 25.03.2007 enrollment Hauptbelegungszeitraum 2 05.03.2007 - 18.03.2007 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Mo.	09:45 to 11:15	woch		Gebäude C - C004		Jobke				
	Thurs.	14:15 to 15:45	woch		Gebäude C - C004						

Functions:

Group [no name]: → application info

Responsible Instructor

Responsible Instructor	Responsibilities	Activity
Jobke, Stephan, Professor a. D., Dr.-Ing.		

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering & Information Technology	1 - 1		5
Bachelor	Applied Computer Science	2 - 2		5
Bachelor	Business Informatics & E-Business	2 - 2		5

Assign to Departments

Electrical Engineering and Information Technology

Contents

Comments	Vorlesungsbeginn ist am Donnerstag, den 15.3.2007
Comment	Am Beispiel moderner 32-Bit Mikroprozessoren (Pentium) erfolgt eine Einführung den Aufbau und die Funktionsweise von Mikroprozessoren. Wichtige serielle und parallele Schnittstellen werden vorgestellt und deren Programmierung erläutert. Der Aufbau prozessorexterne Speicher (Arbeits-Speicher) sowie deren Ansteuerung und Betrieb zusammen mit Pufferspeichern (Cache-Speicher) schließen den schaltungsbezogenen Teil dieses Moduls ab. Der programmtechnische Teil beginnt mit Ausführungen zur Darstellung von Zeichen und Zahlen in einem Rechner. Besondere Betonung findet die Darstellung von gebrochen rationalen Zahlen in Gleitkomma- und Festkomma-Notation. Fortgesetzt wird dieser Teil des Moduls mit einer Einführung in den Assembler-Befehlssatz des Pentiums. Einzelne Programme, die typische Eigenarten der Assemblerprogrammierung illustrieren, werden innerhalb des Moduls entwickelt. Die Kombination von C- und Assemblerprogrammen, die sich insbesondere in der Parameter-Übergabe widerspiegeln bilden den Abschluss dieses Moduls.
Literature	T.E. Padschun; Das Assembler-Buch ; Addison Wesley-Verlag
Remarks	Die Studierenden sind in der Lage: - den Aufbau moderner Rechnersysteme und das Zusammenwirken der wichtigsten Komponenten beurteilen zu können - Zeichen und Zahlen in typischen Darstellungsformen in Mikroprozessor-Systemen zu analysieren und zu verwenden; insbesondere in Implementation bestimmte Genauigkeitsanforderungen bei Zahlen zu beurteilen - das Potential von C-Programmen und Assembler-Programmen gegeneinander abgrenzen zu können - Assemblerprogramme zu entwickeln und von C-Programmen aufrufen zu können.
Access requirements	Kenntnisse in Digitaltechnik
Exams accredited	unmarked: --- . marked: written examination, 90 minutes.

Structure Tree

This lecture was found in SoSe 2007 3 times:

Vorlesungsverzeichnis

- Angewandte Informatik
  - Grundstudium - - - 1
- Wirtschaftsinformatik und eBusiness
  - Grundstudium - - - 2
- Elektrotechnik und Informationstechnik
  - Grundstudium - - - 3

Hint: you are in ***Summer 2007*** and not in the term which is in planning!

Business English 1 - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	lsf-veranstaltung-2113@lists.hs-weingarten.de		
Type of Course	Seminar/ compulsory subject	**Long text**	
Number	2113	**Short text**	
Term	SS 2007	**Hours per week in term**	2
Expected no. of participants		**Max. participants**	20
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	English		
application periods	Hauptbelegungszeitraum 1 26.02.2007 - 11.03.2007 enrollment Hauptbelegungszeitraum 2 05.03.2007 - 18.03.2007 enrollment		

Dates/Times/Location Group: [no name]

Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
Tues.	09:45 to 11:15	woch	from 20.03.2007	Gebäude B - B 020 Labor						

Functions:

Group [no name]: → application info

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering & Information Technology	1 - 1		3

Assign to Departments

Electrical Engineering and Information Technology

CLIC - Center for Languages and Intercultural Communication

Contents

Literature

Pass Cambridge Preliminary
Student s Book (compulsory)
Workbook (optional

Remarks

Business English 1 is based on selected core units from the Pass Cambridge Business English Certificate (BEC) Preliminary Book. This course is not a direct preparation for the Cambridge BEC exam. BE1 is a preparation for a final exam in Business English 1. This exam will consist of a written and listening test at the end of the semester
Content:
The selected core units cover a wide range of business-related topics including: job descriptions and working conditions, telephoning and internal communication, describing trends and talking about company performance and writing letters.
Learning goals:
Development of language skills reading, writing, speaking and listening
systematic and brief revision of grammar
activation of basic functional language phrases
Increase vocabulary.

Structure Tree

This lecture was found in SoSe 2007 2 times:

Vorlesungsverzeichnis
→ Fremdsprachen (fakultätsübergreifend) - - - 1
→ Elektrotechnik und Informationstechnik
→ Grundstudium - - - 2

Hint: you are in ***Summer 2007*** and not in the term which is in planning!

Business English 2 - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	lsf-veranstaltung-2115@lists.hs-weingarten.de		
Type of Course	Seminar/ compulsory subject	**Long text**	
Number	2115	**Short text**	
Term	SS 2007	**Hours per week in term**	2
Expected no. of participants		**Max. participants**	20
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	English		
application periods	Hauptbelegungszeitraum 2 05.03.2007 - 18.03.2007 enrollment Hauptbelegungszeitraum 1 26.02.2007 - 11.03.2007 enrollment		

Dates/Times/Location Group: 1. Group **E-Mailadresse**: lsf-veranstaltung-2115-gruppe-1@lists.hs-weingarten.de

Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
Tues.	11:45 to 13:15	woch	from 20.03.2007	Gebäude B - B 020 Labor						

Functions:

Group 1. Group: → application info

Dates/Times/Location Group: 2. Group **E-Mailadresse**: lsf-veranstaltung-2115-gruppe-2@lists.hs-weingarten.de

Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
Wed.	11:45 to 13:15	woch	from 14.03.2007	Gebäude B - B 020 Labor						

Functions:

Group 2. Group: → application info

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering & Information Technology	2 - 2		2

Assign to Departments

CLIC - Center for Languages and Intercultural Communication

Contents

Literature

Pass Cambridge Preliminary
Student s book; ISBN: 3-526-51121-7 (compulsory)
Workbook (optional)

Remarks

Business English 2 is based on selected core units from the Pass Cambridge Business English Certificate (BEC) Preliminary Book. This course is not a direct preparation for the Cambridge BEC exam. BE2 is a preparation for a final exam in Business English 2. This exam will consist of a written and listening test at the end of the semester
Content:
The selected core units cover a wide range of business-related topics including: arranging a conference, talking about call centres, insurance and changes in working practices, looking at delivery services and trading.
Learning goals:
development of language skills reading, writing, speaking and listening
systematic and brief revision of grammar
activation of basic functional language phrases
increase vocabulary.

Access requirements

Successful completion of BEI (60% pass); 100% attendance

Structure Tree

This lecture was found in SoSe 2007 2 times:

Vorlesungsverzeichnis
- → Fremdsprachen (fakultätsübergreifend) - - - 1
- → Elektrotechnik und Informationstechnik
  - → Grundstudium - - - 2

Hint: you are in ***Summer 2007*** and not in the term which is in planning!

Negotiating - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	lsf-veranstaltung-898@lists.hs-weingarten.de		
Type of Course	Seminar/ compulsory subject	**Long text**	
Number	898	**Short text**	
Term	SS 2007	**Hours per week in term**	2
Expected no. of participants		**Max. participants**	16
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	English		
application periods	Hauptbelegungszeitraum 1 26.02.2007 - 11.03.2007 enrollment Hauptbelegungszeitraum 2 05.03.2007 - 18.03.2007 enrollment Hauptbelegungszeitraum 3 12.03.2007 - 25.03.2007 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Thurs.	09:45 to 11:15	woch	from 15.03.2007	Gebäude B - B 020 Labor						

Functions:

Group [no name]: → application info

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Master mit vorausg. Absch	Social and Health Care Management	-		
Master mit vorausg. Absch	Optical Systems Engineering	-		
Master mit vorausg. Absch	Computer Science	-		

Assign to Departments

CLIC - Center for Languages and Intercultural Communication

Contents

Comments Die Teilnehmerzahl ist auf 16 Studenten begrenzt.
Prioritär sind Masterstudenten und Studenten höherer Semester.
Die endgültige Zulassung wird erst in der zweiten Sitzung vorgenommen.

Remarks This course is designed to further communications skills in the difficult field of negotiating and reaching agreements. Such skills demand understanding of the background to situations and the aims and needs of the parties involved together with the ability to use the level of English required to achieve success. The use of the correct vocabulary, grammatical forms and the formulation of convincing arguments are essential tools in this process.

Structure Tree

This lecture was found in SoSe 2007 1 times:

Vorlesungsverzeichnis
→ Fremdsprachen (fakultätsübergreifend) - - - 1

Course Overview
Course Overview (modularisiert)
Search for Lectures
Time table of study programs
Structure of Curricula modules-LSF
Edit Lecture
Lectures today
Lectures today (with Search)
Hide menu

Hint: you are in ***Summer 2007*** and not in the term which is in planning!

DAF - Single View

Go Back

Functions:

Page contents: Basic Information | Dates/Times/Location | Departments | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-3171@lists.hs-weingarten.de		
Type of Course	---	**Long text**	
Number	3171	**Short text**	
Term	SS 2007	**Hours per week in term**	3
Expected no. of participants		**Max. participants**	
Frequency	Without taking over	**Study Year**	
Hyperlink			
Language	German		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Fr.	13:30 to 15:30	Einzel	at 29.06.2007	Gebäude C - C009						

Functions:

Group [no name]: no applications

Assign to Departments

CLIC - Center for Languages and Intercultural Communication

Structure Tree

Lecture not found in this Term. Lecture is in Term SS 2007 , Currentterm: Summer 2007

Hint: you are in ***Winter 2007/08*** and not in the term which is in planning!

Electrical Engineering 2 - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-2114@lists.hs-weingarten.de		
Type of Course	Lecture/Exercise	**Long text**	
Number	2114	**Short text**	
Term	WS 2007/08	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application period	Hauptbelegungszeitraum 1 17.09.2007 - 30.09.2007		
	enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Mo.	09:45 to 11:15	woch		Gebäude H - H239						
	Tues.	09:45 to 11:15	woch		Gebäude H - H239						

Functions:

Group [no name]: → application info

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering & Information Technology	2 - 2		4

Assign to Departments

Electrical Engineering and Information Technology

Contents

Comment

Calculation of alternating current networks with methods of equivalent circuits
Intensive calculation of average values of alternating periodic parameters
Conversion of series and parallel circuits
Equivalent circuits of coils and capacitors, figure of merit, dissipation factor
Resonant circuits, bandwidth, figure of merit
Active power, reactive power, apparent power, compensation of power matching
Stationary electrical flow field
Electrostatic field
Magnetic field, magnetic circuit
Law of magnetic induction, self-induction, mutual-induction
Equivalent circuit diagrams of transformers with and without dissipation and

Literature

Attia Electronics and Circuit Analysis
Using MATLAB, CRC Press

Remarks

Knowledge of the lecture in electrical engineering 1 is deepened. The equivalent circuits of transformers as well as the calculation of power and the compensation of power matching have been added . The second part deals with fields: the stationary electrical flow field, the electrostatic and the magnetic field. Main emphasis of application are the magnetic circuits which are the basis for the calculation of coils and transformers.

Access requirements

Vorlesung Elektrotechnik 1

Exams accredited

unmarked: practical work.
marked: written examination, 90 minutes.

Information and Communication Engineering (Diploma)
unmarked: --- .
marked: written examination, 120 minutes.
(Hint: will be examined with
Electrical Engineering 1
Electrical Engineering 2)

Structure Tree

This lecture was found in WiSe 2007/08 1 times:

College Catalogue
→ Electrical Engineering and Information Technology
→ Grundstudium - - - 1

Hint: you are in ***Winter 2007/08*** and not in the term which is in planning!

Metrology - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructor | Required subject (SPO) | Contents | Structure Tree

Basic Information

E-Mailadresse	lsf-veranstaltung-2117@lists.hs-weingarten.de		
Type of Course	Lecture	**Long text**	
Number	2117	**Short text**	MESST
Term	WS 2007/08	**Hours per week in term**	2
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application period	Hauptbelegungszeitraum 1 17.09.2007 - 30.09.2007		
	enrollment		

Dates/Times/Location Group: [no name]

Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
Wed.	11:45 to 13:15	woch		Gebäude H - H004		Siggelkow				

Functions:

Group [no name]: → application info

Responsible Instructor

Responsible Instructor	Responsibilities	Activity
Siggelkow, Andreas, Professor, Dr.-Ing.		

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering & Information Technology	2 - 2		2

Contents

Comment

Linear AC networks
Complex variables
Logarithmic level descriptions of signals
Impedance and transfer locus curves
Line spectra of periodic signals
Spectra and Fourier transforms
Transients of linear systems
Laplace transform
Network analysis methods

Literature

Frohne, H.; Löcherer, K.-H.; Müller, H.: Moeller: Grundlagen der Elektrotechnik, Teubner, Stuttgart 2002.
Führer, A.; Heidemann, K.; Nerreter, W.: Grundgebiete der Elektrotechnik, Hanser, München 1990.
Werner, M.: Signale und Systeme, Vieweg, Braunschweig 2000.

Remarks

Using Fourier analysis (series and transforms), we consider the currents and voltages in circuits of lumped RLC elements. We demonstrate the relationship between signals of arbitrary time dependence and their spectra. For mathematical elegance we treat the dynamic transients of linear systems with Laplace transform in operator space. In addition, impedance and transfer locus curves are discussed as well as algorithmic network analysis methods and logarithmic level descriptions of signals.

Exams accredited

unmarked: --- .
marked: written examination, 60 minutes.

Structure Tree

This lecture was found in WiSe 2007/08 1 times:

College Catalogue
→ Electrical Engineering and Information Technology
→ Grundstudium - - - 1

Hint: you are in ***Winter 2007/08*** and not in the term which is in planning!

Mathematics 2: Analysis / Linear Algebra 2 - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructor | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	lsf-veranstaltung-4057@lists.hs-weingarten.de		
Type of Course	Lecture	**Long text**	
Number	4057	**Short text**	
Term	WS 2007/08	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application period	Hauptbelegungszeitraum 1 17.09.2007 - 30.09.2007		
	enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Tues.	08:00 to 09:30	woch		Gebäude H - H142		Fechter				
	Fr.	08:00 to 09:30	woch		Gebäude H - H239						

Functions:

Group [no name]: → application info

Responsible Instructor

Responsible Instructor	Responsibilities	Activity
Fechter, Frank, Professor, Dr.-Ing.		

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Physical Engineering	2 - 2		5
Bachelor	Electrical Engineering & Information Technology	2 - 2		

Assign to Departments

Physical Engineering

Contents

Comment	13. Line integral 14. Limit values and discontinuities 15. Relations and mappings (generalization of function definition) 16. Complex functions 17. Linear algebra
Literature	R.A. Adams Calculus : a complete course, Addison-Wesley Publ. (1995)
Remarks	Aim of the lecture is to enable students to apply the mathematical methods of this course by themselves so that they may master technical lectures with a strong mathematical background. The topics of this course are given as follows : Extension of integration to contour integrals as regards to scalar and vector fields. Different methods to calculate limit values of indefinite expressions will be discussed in detail. The definition of functions will be deepened; examples for explicit, implicit and parametric functions will be given. Moreover, general (i.e. local and global) properties of real-valued functions will be investigated. Complex analysis will be continued with the discussion of complex-valued functions. In this context elementary algebraic, transcendental, trigonometric and hyperbolic functions and the corresponding inverse functions will be treated. Applications of complex functions of real variables are the treatment of AC-circuits and Nyquist plots in the complex plane. As to complex functions of complex variables there the Cauchy-Riemann differential equations will be treated and, moreover, conformal mapping illustrated by several examples such as the Joukowski transform. Objectives of linear algebra are determinants and matrices as well as the determination of the inverse of a matrix. The calculation of eigen-values and associated eigen-vectors of matrices given will be applied on the one hand to the calcuation of eigen-modes of oscillatory systems and on the other hand to the evaluation of principal axes (diagonalization of quadrics and calculation of the ellipsoid of moments of inertia). Finally, linear systems of equations will be solved by means of Gaussian elimination method.
Access requirements	Mathematik 1
Exams accredited	unmarked: --- . marked: written examination, 90 minutes.

Structure Tree

This lecture was found in WiSe 2007/08 1 times:

College Catalogue
→ Electrical Engineering and Information Technology
→ Grundstudium - - - 1

Hint: you are in ***Winter 2007/08*** and not in the term which is in planning!

Programming - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructor | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-1804@lists.hs-weingarten.de		
Type of Course	Lecture	**Long text**	
Number	1804	**Short text**	
Term	WS 2007/08	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 1 17.09.2007 - 30.09.2007 enrollment Hauptbelegungszeitraum 3 01.10.2007 - 14.10.2007 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Fr.	14:15 to 15:45	Einzel	at 26.10.2007 Abschlussfeier Fakultäten M & T	Gebäude H - H061 (max. 30 Personen)						
	Fr.	14:15 to 15:45	Einzel	at 02.11.2007	Gebäude H - H061 (max. 30 Personen)						
	Wed.	14:15 to 15:45	woch		Gebäude C - C004		Zeller				
	Thurs.	14:15 to 15:45	woch		Gebäude C - C004						

Functions:

Group [no name]: → application info

Responsible Instructor

Responsible Instructor	Responsibilities	Activity
Zeller, Martin, Professor, Dr. rer. nat.		

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Applied Computer Science	1 - 1	mandatory subject	5
Bachelor	Electrical Engineering & Information Technology	2 - 2		5
Bachelor	Business Informatics & E-Business	1 - 1		5

Assign to Departments

Applied Computer Science

Contents

Comment

The lectures adresses the following topics:
- introduction to computer, operating systems and compilers
- basic concepts of the C programming language (main-function, varables, operators, loops, ...)
- functions, parameter passing
- complex data types (arrays, structs, pointer)
- dynamic memory management
- file I/O
- recursion

In the practical part of the course the students solve several programming tasks each aiming on one or two topics of the lecture (see above).

Literature

Schildt, Herbert: C: The Complete Reference . Osborne, McGraw-Hill, 2000. (ISBN 0-07-212124-6)

Remarks

The lecture covers basic procedural programming in the C programming language. Students will learn to solve simple programming tasks and to analyse a given program.

Exams accredited

unmarked: --- .
marked: written examination, 90 minutes.

Applied Computer Science (Diploma)
unmarked: --- .

marked: written examination, 120 minutes.
(Hint: will be examined with
Programming
Programming practical part)

Structure Tree

This lecture was found in WiSe 2007/08 3 times:

College Catalogue
- Applied Computer Science
  - Grundstudium - - - 1
- Electrical Engineering and Information Technology
  - Grundstudium - - - 2
- Business Information and E-Business
  - Grundstudium - - - 3

Hint: you are in *Winter 2007/08* and not in the term which is in planning!

Programming, Practical training - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructors | Required subject (SPO) | Contents | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-1806@lists.hs-weingarten.de		
Type of Course	Practical training	**Long text**	
Number	1806	**Short text**	PROGP
Term	WS 2007/08	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 1 17.09.2007 - 30.09.2007 enrollment Hauptbelegungszeitraum 3 01.10.2007 - 14.10.2007 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Mo.	14:15 to 15:45	woch		Gebäude K - K 102		Zeller , Perk				
	Mo.	16:00 to 17:30	woch		Gebäude K - K 102						

Functions:

Group [no name]: → application info

Responsible Instructors

Responsible Instructors	Responsibilities	Activity
Zeller, Martin, Professor, Dr. rer. nat.		
Perk, Norbert , Dipl.Ing.(TH)		

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering & Information Technology	2 - 2		5

Contents

Comment	In the practical part of the course is split into groups of 20 - 25 students per classroom. The students solve several programming tasks each aiming on one or two topics of the theoretical part of the course. The result of each programming task is the programm and a documentation. The results are checked whether the meet a certain level otherwise the task has to be reworked. That is, students get feedback in the classroom as well as in writing.
Literature	Schildt, Herbert: C: The Complete Reference . Osborne, McGraw-Hill, 2000. (ISBN 0-07-212124-6)
Remarks	The lecture covers basic procedural programming in the C programming language. Students will learn to solve simple programming tasks and to analyse a given program. Additionally to the theoretical part of the course, students will learn to work with an integrated development environment.
Exams accredited	unmarked: practical work. marked: --- .

Structure Tree

This lecture was found in WiSe 2007/08 1 times:

College Catalogue
→ Electrical Engineering and Information Technology
→ Grundstudium - - - 1

Hint: you are in ***Winter 2007/08*** and not in the term which is in planning!

Processor technology, Practical training - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructor | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	lsf-veranstaltung-1809@lists.hs-weingarten.de		
Type of Course	Practical training	**Long text**	
Number	1809	**Short text**	RTECP
Term	WS 2007/08	**Hours per week in term**	2
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 1 17.09.2007 - 30.09.2007 enrollment Hauptbelegungszeitraum 2 24.09.2007 - 07.10.2007 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Tues.	14:15 to 15:45	woch		Gebäude H - H 216		Jobke				
	Tues.	16:00 to 17:30	woch		Gebäude H - H 216						

Functions:

Group [no name]: → application info

Responsible Instructor

Responsible Instructor	Responsibilities	Activity
Jobke, Stephan, Professor a. D., Dr.-Ing.	responsible	

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering & Information Technology	2 - 2		

Assign to Departments

Electrical Engineering and Information Technology

Contents

Comments
!!!! Einführungsveranstaltung am Dienstag, 09.10.2007 im H216 ist Pflicht für ALLE !!!

Da gibt es auch die Unterlagen für die Versuche.

Comment
In praktischen Beispielen wird die Assemblerprogrammierung basierend auf den Kenntnissen aus dem Modul "Rechnertechnologie" angewandt. Die zu entwickelnden Assemblerprogramme sind so konzipiert, dass sie von einem C-Programm aufgerufen werden, indem die Umgebung für das Assemblerprogramm bereitgestellt wird (Ein- und Ausgabe von Testdaten). Die Handhabung von Debuggern (Sourcelevel-Debugger) wird erläutert und praktiziert. Die für die Übersetzung von Programmen notwendigen Schritte (gcc-Compiler für C-Programme, NASM für Assembler-Programme) werden erklärt und in einer speziellen Kommando-Datei abgelegt (Makefile).

Literature
T.E. Padschun; Das Assembler-Buch ; Addision Wesley-Verlag

Remarks
Die Studierenden sind in der Lage:
- Assemblerprogramme für moderne 32-Bit Mikroprozessoren (Pentium) entwickeln, übersetzen und testen zu können
- Assembler-Programme als C-Funktionen zu konzipieren und die Parameterübergabe in beiden Richtungen (C zu Assembler und umgekehrt) programmieren zu können
- Übersetzungswerkzeuge zu programmieren und einsetzen zu können (Programmentwicklungsumgebung, "Make"-Prozedur).

Exams accredited
unmarked: practical work.
marked: --- .

Structure Tree

This lecture was found in WiSe 2007/08 1 times:

College Catalogue
→ Electrical Engineering and Information Technology
→ Grundstudium - - - 1

Hint: you are in *Winter 2007/08* and not in the term which is in planning!

Computer networks - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	lsf-veranstaltung-1830@lists.hs-weingarten.de		
Type of Course	Lecture	**Long text**	
Number	1830	**Short text**	
Term	WS 2007/08	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 1 17.09.2007 - 30.09.2007 enrollment Hauptbelegungszeitraum 3 01.10.2007 - 14.10.2007 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Fr.	11:45 to 13:15	Einzel	at 19.10.2007 **Abschlussfeier Fakultäten E & S**	Gebäude D - D002						
	Fr.	11:45 to 13:15	Einzel	at 26.10.2007 **Abschlussfeier Fakultäten M & T**	Gebäude D - D002						
	Fr.	11:45 to 13:15	woch		Gebäude C - C004						
	Wed.	16:00 to 17:30	woch		Gebäude C - C004						

Functions:

Group [no name]: → application info

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering & Information Technology	2 - 2		5
Bachelor	Business Informatics & E-Business	3 - 3		5
Bachelor	Applied Computer Science	3 - 3		5

Assign to Departments

Applied Computer Science

Contents

Comment	LAN-Technologien: IEEE 802.3/Ethernet, Fast Ethernet, Gigabit Ethernet, Internetadressen, Bildung von Subnetzen, Routing von IP- Paketen, VLANs, Auflösung von Namen Protokolle : IP, ICMP, ARP, TCP, UDP, RIP Adresskonzepte: DHCP, Nat, CIDR LAN-Netzwerkkomponenten: Kabel, Hub, Bridge, Switch, Router, Gateway Netzanalyse mit Hilfe von Protokollanalysatoren.
Literature	Vorlesungslückenskript; Tanenbaum, A.: "Computernetzwerke", Prentice Hall (2003), ISBN: 3-8273-7046-9; Stein: Taschenbuch Rechnernetze und Internet, Hanser Fachbuchverlag (2003), ISBN: 3446225730; Washburn/Evans: Aufbau und Betrieb eines TCP/IP-Netzes, Addison-Wesley (1994), ISBN:3-893 19-658-7
Remarks	Im Fach Netzwerktechnologien soll ein Verständnis für die Arbeitsweise von aktiven LAN-Komponenten vermittelt werden. Es soll ferner gezeigt werden, wie Workstations hinsichtlich ihrer Netzwerkeigenschaften zu konfigurieren sind und wie Fehler in der Konfiguration gefunden werden können.
Exams accredited	unmarked: --- . marked: written examination, 90 minutes.

Structure Tree

This lecture was found in WiSe 2007/08 3 times:

College Catalogue
- → Applied Computer Science
  - → Grundstudium - - - 1
- → Electrical Engineering and Information Technology
  - → Grundstudium - - - 2
- → Business Information and E-Business
  - → Grundstudium - - - 3

Hint: you are in ***Summer 2008*** and not in the term which is in planning!

Electrical Engineering 3 - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructor | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-2123@lists.hs-weingarten.de		
Type of Course	Lecture	**Long text**	
Number	2123	**Short text**	
Term	SS 2008	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 2 03.03.2008 - 16.03.2008 enrollment Hauptbelegungszeitraum 1 25.02.2008 - 09.03.2008 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Thurs.	09:45 to 11:15	woch		Gebäude H - H238		Kark				
	Thurs.	11:45 to 13:15	woch		Gebäude H - H238						

Functions:

Group [no name]: → application info

Responsible Instructor

Responsible Instructor	Responsibilities	Activity
Kark, Klaus Werner, Professor, Dr.-Ing.		

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering & Information Technology	3 - 3		5

Assign to Departments

Electrical Engineering and Information Technology

Contents

Comments	The lecture starts on 13. March 2008.
Comment	Linear AC networks Complex variables Logarithmic level descriptions of signals Impedance and transfer locus curves Line spectra of periodic signals Spectra and Fourier transforms Transients of linear systems Laplace transform Network analysis methods
Literature	Albach, M.: Grundlagen der Elektrotechnik 2, Pearson, München 2005. Frohne, H.; Löcherer, K.-H.; Müller, H.: Moeller: Grundlagen der Elektrotechnik, Teubner, Stuttgart 2002. Führer, A.; Heidemann, K.; Nerreter, W.: Grundgebiete der Elektrotechnik, Hanser, München 1990. Philippow, E.: Grundlagen der Elektrotechnik, Hüthig, Heidelberg 1989. Scheithauer, R.: Signale und Systeme, Teubner, Stuttgart 1998. Weber, H.: Laplace-Transformation, Teubner, Stuttgart 1990. Werner, M.: Signale und Systeme, Vieweg, Wiesbaden 2000. Mildenberger O.: System- und Signaltheorie, Vieweg, Wiesbaden 1989.
Remarks	Using Fourier analysis (series and transforms), we consider the currents and voltages in circuits of lumped RLC elements. We demonstrate the relationship between signals of arbitrary time dependence and their spectra. For mathematical elegance we treat the dynamic transients of linear systems with Laplace transform in operator space. In addition, impedance and transfer locus curves are discussed as well as algorithmic network analysis methods and logarithmic level descriptions of signals.
Access requirements	Electrical Engineering 1+2 Mathematics 1+2
Exams accredited	marked: written examination, 90 minutes.

Structure Tree

This lecture was found in SoSe 2008 1 times:

Hint: you are in ***Summer 2008*** and not in the term which is in planning!

Metrology, Practical training - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-2122@lists.hs-weingarten.de		
Type of Course	Exercise	**Long text**	
Number	2122	**Short text**	
Term	SS 2008	**Hours per week in term**	2
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering & Information Technology	3 - 3		2

Assign to Departments

Electrical Engineering and Information Technology

Contents

Comment

Zunächst werden grundlegende Begriffe wie Maßeinheiten, Messfehler und Messnormale behandelt. Anschließend wird die Funktion von Messgeräten und Messschaltungen unterrichtet. Beispiele sind die elektromechanischen Messgeräte, digitale Messgeräte, Messgleichrichter und insbesondere das Elektronenstrahloszilloskop. Es folgt eine Beschreibung grundlegender Messverfahren für Gleichspannung und Gleichstrom, Wechselspannung und Wechselstrom, elektrische Leistung, Phasenunterschiede sowie Gleich- und Wechselstromwiderstände. Ein Kapitel über Störeinflüsse und Gegenmaßnahmen führt in die Messung kleiner Spannungen und Ströme ein. Den Abschluss bildet ein Kapitel über den IEC-Bus.

Literature

Bergmann, K.: Elektrische Messtechnik, Vieweg, 1997
Hoffmann, J.: Taschenbuch der Messtechnik, Fachbuchverlag Leipzig, 1998
Lerch: Elektrische Messtechnik, Springer, 1996
Schwetlick, H.: PC-Messtechnik, Vieweg, 1997
Meyer, G.: Oszilloskope, Hüthig, 1996
Schmusch, W.: Elektronische Messtechnik. Vogel-Verlag, 1990
Schrüfer, E.: Elektrische Messtechnik. Hanser, 1994
Profos; Pfeifer: Grundlagen der Messtechnik. Oldenbourg 1997
Richter, W.: Elektrische Messtechnik, VDE-Verlag 1994
Stöckl; Winterling: Elektrische Messtechnik, Teubner, 1987
T. Mühl: Einführung in die elektrische Messtechnik, Teubner 2001
R. Felderhoff, U. Freyer: Elektrische und elektronische Messtechnik, Hanser 2003
R. Parthier: Messtechnik, Vieweg 2001
E. Schrüfer: Elektrische Messtechnik 2004
B. Schiek: Grundlagen der Hochfrequenzmesstechnik, Hüthig 1998
U. Tietze, Ch. Schenk: Halbleiter-Schaltungstechnik, Springer 2002
A. Schwab: Elektromagnetische Verträglichkeit, Springer 1996
M. Zahn, S. Osterrieder: Elektrische Messtechnik, Vorlesungsskript

Remarks

Die Messtechnik ist ein Grundlagengebiet in der Naturwissenschaft und Technik. Viele Sachverhalte lassen sich nur mit Hilfe von Messungen exakt beschreiben. Die Studierenden sollen in dieser Vorlesung mit der Systematik beim Messen vertraut gemacht werden. Sie erlernen theoretische Grundlagen der Messtechnik und erhalten eine Einführung in wichtige elektrische Messverfahren und Messschaltungen.

Access requirements

Elektrotechnik 1

Exams accredited

Unbenotete Prüfungsleistung: Praktische Arbeit.
Benotete Prüfungsleistung: --- .

Structure Tree

Lecture not found in this Term. Lecture is in Term SS 2008 , Currentterm: Summer 2008

Hint: you are in ***Summer 2008*** and not in the term which is in planning!

Mathematics 3 - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructor | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-2105@lists.hs-weingarten.de		
Type of Course	Lecture	**Long text**	
Number	2105	**Short text**	
Term	SS 2008	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 2 03.03.2008 - 16.03.2008 enrollment Hauptbelegungszeitraum 1 25.02.2008 - 09.03.2008 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Thurs.	08:00 to 09:30	woch		Gebäude H - H002						
	Tues.	09:45 to 11:15	woch		Gebäude B - B 309		Siggelkow				

Functions:

Group [no name]: → application info

Responsible Instructor

Responsible Instructor	Responsibilities	Activity
Siggelkow, Andreas, Professor, Dr.-Ing.		

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering & Information Technology	3 - 3		5

Assign to Departments

Electrical Engineering and Information Technology

Contents

Comment	Content is: Fourier Series Fourier Transformation Laplace Transformation Theory of Probabilities Statistics
Literature	Bracewell, R.N.: The Fourier transform and its applications, McGraw-Hill, New York 1986. Jackson, L.B.: Signals, Systems and Transforms, Addison-Wesley, Reading MA 1991.
Remarks	The lecture mathematics 3 is a collection of important mathematical fundmentals, which are pre condition for other lectures in electrical engineering and computer sciences. This includes Fourier Series, Fourier Transformation, Laplace Transformation, Theory of Probabilities and Statistics.
Access requirements	Mathematics 1 & 2
Exams accredited	marked: written examination, 90 minutes.

Structure Tree

This lecture was found in SoSe 2008 1 times:

University course catalog
→ **Electrical Engineering & Information Te (Bachelor) ET**
→ **Stage I studies** - - - 1

Hint: you are in ***Summer 2008*** and not in the term which is in planning!

Electronics 1 - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-1815@lists.hs-weingarten.de		
Type of Course	Lecture	**Long text**	
Number	1815	**Short text**	
Term	SS 2008	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 2 03.03.2008 - 16.03.2008 enrollment Hauptbelegungszeitraum 1 25.02.2008 - 09.03.2008 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Fr.	08:00 to 09:30	woch		Gebäude H - H002						
	Fr.	09:45 to 11:15	woch		Gebäude H - H002						

Functions:

Group [no name]: → application info

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering & Information Technology	3 - 3		5

Assign to Departments

Electrical Engineering and Information Technology

Contents

Comments	Vorlesungs-Start: Freitag, 28.03.2008, 8:00 Uhr, Raum H002
Comment	Operational Amplifier: Ideal and real amplifier, reverse and non-reverse amplifier, summation and subtraction, integrator and differentiator, transfer function, time response, input and output resistance. Filter: Low-pass, high-pass, band-pass, band-stop and all-pass filters. Diodes and Z-Diodes: Characterictics, differential resistance, operating point, dependency of temperature, circuits for clipping voltages (stabilization). Bipolar-Transistors: Characterictics, allowed operating range, choice of operating point, add-on circuit to adjust the operating point, DC-circuit-diagramm, DC-working straight, AC-circuit-diagramm, AC-working line, small signal parameters. Basic Circuits with transistors: Emitter-circuit, basis-circuit, collector-circuit, adjusting the operating point, circuits for DC and AC, transfer function, input and output resistance, derivation of current-sources and current reflector.
Literature	Gossner, Stefan: Grundlagen der Elektronik. 3. Auflage, Shaker -Verlag Tietze,Schenk: Halbleiterschaltungstechnik: 11. Auflage, Springer-Verlag
Remarks	Basic components, typical fundamental circuits and elementary Methods of analysis are regarded. Basic components are ideal and real (operational)-amplifier or discrete units as diodes, MOS- and bipolar transistors. Basic circuits and filters contain normally one active basic device. The charcteristics of the circuit will be analysed in time-domain and frequency domain manually and by using computer-aided analysis methods as MATLAB and PSPICE.
Exams accredited	unmarked: --- . marked: written examination, 90 minutes.

Structure Tree

This lecture was found in SoSe 2008 1 times:

University course catalog
→ Electrical Engineering & Information Te (Bachelor) ET
→ Stage I studies - - - 1

Hint: you are in ***Summer 2008*** and not in the term which is in planning!

Electronics, Practical training - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructors | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-1816@lists.hs-weingarten.de		
Type of Course	Practical training	**Long text**	
Number	1816	**Short text**	
Term	SS 2008	**Hours per week in term**	2
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 2 03.03.2008 - 16.03.2008 enrollment Hauptbelegungszeitraum 1 25.02.2008 - 09.03.2008 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Tues.	14:15 to 15:45	woch	from 18.03.2008	Gebäude H - H 227		Reusch				
	Tues.	16:00 to 17:30	woch	from 18.03.2008	Gebäude H - H 227						

Functions:

Group [no name]: → application info

Responsible Instructors

Responsible Instructors	Responsibilities	Activity
Reusch, Rainer	responsible	
Weber, Christoph , Dipl.Ing.(FH)	attendant	

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering & Information Technology	3 - 3		2

Assign to Departments

Electrical Engineering and Information Technology

Contents

Comment	Test "Passive Circuits" Test "Ohmic Resistance" Test "Thévenin Eqivalent Generators" Tes "Mean Values" Test "RC High-Pass" Test "RC Low-Pass" Test "RC Band-Pass" Test "Low-Pass with Operational Amplifiers" Test "High-Pass with Operational Amplifiers" Test "Band-Pass with Operational Amplifiers"
Remarks	The practical training Electrical Engineering/Electronics supports and goes with the lectures "Electrical Engineering 1" and "fundamental Principles of Electronics" including lab practice. In "Electrical Engineering" the student examines and takes a closer look at discrete component ports like e.g. resistors, capacitors, coils or signal generators and their use in passive circuits. "Electronics" deals with the behaviour of active components in relevant basic circuits. Talking about active components e.g. transistors or operational amplifiers are referred to. The "practical Training Electrical Engineering/ Electronics" contains at least two tests belonging to the areas "Electrical Engineering and Electronics".
Access requirements	Unbenotete Prüfungsleistung: Praktische Arbeit. Benotete Prüfungsleistung: --- .
Exams accredited	unmarked: practical work. marked: --- .

Structure Tree

This lecture was found in SoSe 2008 1 times:

University course catalog
→ Electrical Engineering & Information Te (Bachelor) ET
→ Stage I studies - - - 1

Hint: you are in ***Summer 2008*** and not in the term which is in planning!

Communication networks - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructor | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-1819@lists.hs-weingarten.de		
Type of Course	Lecture	**Long text**	
Number	1819	**Short text**	
Term	SS 2008	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 2 03.03.2008 - 16.03.2008 enrollment Hauptbelegungszeitraum 1 25.02.2008 - 09.03.2008 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Mo.	08:00 to 09:30	woch		Gebäude B - B 309		Fechter				
	Thurs.	16:00 to 17:30	woch		Gebäude B - B 309						

Functions:

Group [no name]: → application info

Responsible Instructor

Responsible Instructor	Responsibilities	Activity
Fechter, Frank, Professor, Dr.-Ing.		

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering & Information Technology	3 - 3		5

Assign to Departments

Electrical Engineering and Information Technology

Contents

Comment	In diesem Modul wird zunächst das ISO/OSI 7 Schichtenmodell behandelt, und das Konzept von Diensten, Diensterbringern und Dienstnutzern wird erläutert. Anschließend wird auf die Schichten im Einzelnen eingegangen. Die physikalische Schicht wird nur kurz behandelt, da Elemente der physikalischen Schicht Gegenstand anderer nachrichtentechnischer Vorlesungen sind. Einen Schwerpunkt der Vorlesung bildet die Behandlung der Sicherungsschicht. Die Grundlagen der Fehlersicherung sowie ausgewählte Codes werden ebenso behandelt wie ARQ-Verfahren. Ein Kapitel zur Vermittlungsschicht bildet den zweiten Schwerpunkt der Vorlesung. Über die höheren Schichten des ISO/OSI-Modells wird nur ein Überblick vermittelt. Zur Vertiefung der Grundlagen werden ausgewählte Netztechnologien im Detail vorgestellt. Diese Netzbeispiele werden aus dem Gebiet der drahtgebundenen Weitverkehrsnetze genommen, da die lokalen Rechnernetze und die Mobilfunknetze in anderen Vorlesungen behandelt werden. Damit erhält der Studierende im Laufe seines Studiums einen Überblick über alle wichtigen Netztechnologien. Behandelt werden in der Vorlesung Kommunikationsnetze: ATM, ISDN, SDH, Frame Relay, SS7, Intelligentes Netz und breitbandige Zugriffsverfahren.
Literature	Tanenbaum, A. S.: Computer Networks. Prentice Hall, New Jersey, 2003.
Remarks	Kommunikationsnetze werden nicht nur für die Sprachkommunikation benötigt, sondern auch für die Rechnerkommunikation. Aufgrund permanenter Weiterentwicklungen der Technologien ist dieses Fachgebiet einem schnellen Wandel unterworfen und bietet stets neue Herausforderungen für den im Berufsleben stehenden Ingenieur. Das Ziel dieser Vorlesung ist, Grundlagenwissen über Kommunikationsnetze zu vermitteln, auf das nachfolgende Vorlesungen aufbauen können. Diese Grundlagen sollen dem Studierenden aber auch helfen, sich nach seinem Studium selbständig mit den neusten Entwicklungen des Faches vertraut zu machen.
Access requirements	Mathematik 1
Exams accredited	unmarked: --- . marked: written examination, 90 minutes.

Structure Tree

This lecture was found in SoSe 2008 1 times:

University course catalog
→ Electrical Engineering & Information Te (Bachelor) ET
→ Stage I studies - - - 1

Hint: you are in ***Summer 2008*** and not in the term which is in planning!

Netzwerktechnologien Praktikum - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructor | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	lsf-veranstaltung-1908@lists.hs-weingarten.de		
Type of Course	Practical training	**Long text**	
Number	1908	**Short text**	
Term	SS 2008	**Hours per week in term**	2
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application period	Hauptbelegungszeitraum 1 25.02.2008 - 09.03.2008		
	enrollment		

Dates/Times/Location Group: 1. Group E-Mailadresse: lsf-veranstaltung-1908-gruppe-1@lists.hs-weingarten.de

Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
Wed.	14:15 to 15:45	woch		Gebäude C - C 121						20
Wed.	14:15 to 15:45	woch		Gebäude C - C 122						

Functions:

Group 1. Group: → application info

Dates/Times/Location Group: 2. Group E-Mailadresse: lsf-veranstaltung-1908-gruppe-2@lists.hs-weingarten.de

Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
Fr.	11:45 to 13:15	woch		Gebäude C - C 121						20
Fr.	11:45 to 13:15	woch		Gebäude C - C 122						

Functions:

Group 2. Group: → application info

Responsible Instructor

Responsible Instructor	Responsibilities	Activity
Perk, Norbert , Dipl.Ing.(TH)		

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Study focus AI-Multimedia-Engineering	4 - 4		
Bachelor	Study focus AI-Information networks	4 - 4		
Bachelor	Study focus AI-E-Business	4 - 4		
Bachelor	Study focus AI-Automation systems	4 - 4		
Bachelor	Electrical Engineering & Information Technology	3 - 3		3

Assign to Departments

Applied Computer Science

Contents

Comment	- integration of work stations into networks - setup of a structured cabling infrastructure - experiment with spanning tree protocol - setup of a VLAN topology - configuration of routers - examination of routing protocols
Literature	lecture notes networktechnologies
Remarks	Deepening of the lecture contents "Network Technologies"
Access requirements	Lecture Networktechnologies
Exams accredited	unmarked: --- . marked: laboratory work.

Structure Tree

This lecture was found in SoSe 2008 6 times:

University course catalog
- Electrical Engineering & Information Te (Bachelor) ET
  - Stage I studies --- 1
- Applied Computer Science (Bachelor) AI
  - Main field of study: Automation/Multimedia-Systems (Bachelor) --- 2
  - Main field of study: Information Networks --- 3
  - Main field of study: Information Networks/E-Business (Bachelor) --- 4
  - Main field of study: Information Networks/Multimedia-Systeme (Bachelor) --- 5
  - Main field of study: Multimedia-Systems/E-Business --- 6

Hint: you are in ***Summer 2008*** and not in the term which is in planning!

Circuit design - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructors | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-1910@lists.hs-weingarten.de		
Type of Course	Lecture	**Long text**	
Number	1910	**Short text**	
Term	SS 2008	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 2 03.03.2008 - 16.03.2008 enrollment Hauptbelegungszeitraum 1 25.02.2008 - 09.03.2008 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Thurs.	14:15 to 15:45	woch		Gebäude H - H238		Ludescher				
	Thurs.	16:00 to 17:30	woch		Gebäude H - H238						

Functions:

Group [no name]: → application info

Responsible Instructors

Responsible Instructors	Responsibilities	Activity
Ludescher, Walter, Professor a. D., Dr.-Ing.		
Meier, Martin , Dipl.Ing.(FH)		

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Study focus KT-Communication technology	4 - 4		5
Bachelor	Study focus ET-Automation technology	4 - 4		5

Assign to Departments

Electrical Engineering and Information Technology

Contents

Comment

Das Modul Rechnergestützter Schaltungsentwurf stellt moderne Werkzeuge und Methoden zum Entwurf komplexer digitaler Systeme oder hochintegrierter Schaltungen vor. Grundlage zu Schaltungsentwurf /CAE sind die Vorlesungen zur Digitaltechnik. Vorlesungsbegleitende Übungen am CAE-System behandeln den Entwurf kombinatorischer und sequenzieller Logik. Im Verlauf der Veranstaltung werden Übungsbeispiele an CAE-Workstations eingegeben, simuliert und ausgewertet. Digitale Logikschaltungen werden mit modernen Simulationswerkzeugen auf Funktion und Zeitverhalten analysiert, die technische Realisierbarkeit der Schaltung wird durch Fehlersimulation und Prüfmustergenerierung sichergestellt.

Inhalt:
Einführung in die digitale Schaltungstechnik
Logikfamilien und deren Eigenschaften
Logiksimulation auf der System- und der Gatter-Ebene
Schaltungs- und Systemsimulation, VHDL
IC-Test Beobachtbarkeit und Kontrollierbarkeit
Schaltungssynthese und Personalisierung am Beispiel von FPGA und Gate-Array

Literature

siehe Script / see script / siehe Script

Remarks

Kennenlernen von modernen Werkzeugen und Methoden zum Entwurf komplexer digitaler Systeme oder hochintegrierter Schaltungen.

Access requirements

Digital Electronics

Exams accredited

unmarked: --- .
marked: scientific paper.

Structure Tree

This lecture was found in SoSe 2008 2 times:

University course catalog
→ Electrical Engineering & Information Te (Bachelor) ET
→ Stage II studies and study focus Automation - - - 1
→ Main field of study: Communication - - - 2

Hint: you are in ***Summer 2008*** and not in the term which is in planning!

Circuit design, Practical training - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructor | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	lsf-veranstaltung-1911@lists.hs-weingarten.de		
Type of Course	Practical training	**Long text**	
Number	1911	**Short text**	
Term	SS 2008	**Hours per week in term**	2
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 2 03.03.2008 - 16.03.2008 enrollment Hauptbelegungszeitraum 1 25.02.2008 - 09.03.2008 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Thurs.	09:45 to 11:15	woch		Gebäude H - H 214		Siggelkow				
	Thurs.	11:45 to 13:15	woch		Gebäude H - H 214						

Functions:

Group [no name]: → application info

Responsible Instructor

Responsible Instructor	Responsibilities	Activity
Siggelkow, Andreas, Professor, Dr.-Ing.	responsible	

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Study focus ET-Automation technology	4 - 4		2
Bachelor	Study focus KT-Communication technology	4 - 4		2

Assign to Departments

Electrical Engineering and Information Technology

Contents

Comment	1) Simulation of circuits 2) Circuit and system simulation with VHDL 3) Fault simulation and test anlysis 4) Synthesis and backend (FPGA) 5) Verification and test
Remarks	Digital circuit design with VHDL This practical work will be supportet by the lecture "Computer Aided Circuit Design"
Exams accredited	unmarked: practical work. marked: --- .

Structure Tree

This lecture was found in SoSe 2008 2 times:

University course catalog
→ Electrical Engineering & Information Te (Bachelor) ET
→ Stage II studies and study focus Automation - - - 1
→ Main field of study: Communication - - - 2

Hint: you are in ***Winter 2008/09*** and not in the term which is in planning!

Circuit design, Practical training - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructors | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	lsf-veranstaltung-1911@lists.hs-weingarten.de		
Type of Course	Practical training	**Long text**	
Number	1911	**Short text**	
Term	WS 2008/09	**Hours per week in term**	2
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 1 22.09.2008 - 05.10.2008 enrollment Hauptbelegungszeitraum 2 29.09.2008 - 12.10.2008 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Thurs.	11:45 to 13:15	woch		Gebäude H - H 214		Ludescher				
	Thurs.	14:15 to 15:45	woch		Gebäude H - H 214						

Functions:

Group [no name]: → application info

Responsible Instructors

Responsible Instructors	Responsibilities	Activity
Ludescher, Walter, Professor a. D., Dr.-Ing.	responsible	
Weber, Christoph , Dipl.Ing.(FH)	attendant	

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Study focus KT-Communication technology	4 - 4		2
Bachelor	Study focus ET-Automation technology	4 - 4		2

Assign to Departments

Electrical Engineering and Information Technology

Contents

Comment	1) Simulation of circuits 2) Circuit and system simulation with VHDL 3) Fault simulation and test anlysis 4) Synthesis and backend (FPGA) 5) Verification and test
Remarks	Digital circuit design with VHDL This practical work will be supportet by the lecture "Computer Aided Circuit Design"
Exams accredited	unmarked: practical work. marked: --- .

Structure Tree

This lecture was found in WiSe 2008/09 2 times:

- University course catalog
  - → Electrical Engineering & Information Te (Bachelor) ET
    - → Stage II studies and study focus Automation - - - 1
    - → Main field of study: Communication - - - 2

Hint: you are in ***Winter 2008/09*** and not in the term which is in planning!

Digital signal processing - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructor | Required subject (SPO) | Contents | Structure Tree

Basic Information

E-Mailadresse	lsf-veranstaltung-2152@lists.hs-weingarten.de		
Type of Course	Lecture	**Long text**	
Number	2152	**Short text**	
Term	WS 2008/09	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 1 22.09.2008 - 05.10.2008 enrollment Hauptbelegungszeitraum 2 29.09.2008 - 12.10.2008 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Tues.	09:45 to 11:15	woch		Gebäude H - H143						
	Tues.	09:45 to 11:15	woch		Gebäude H - H104						
	Mo.	11:45 to 13:15	woch		Gebäude H - H239						
	Mo.	11:45 to 13:15	woch		Gebäude H - H104						

Functions:

Group [no name]: → application info

Responsible Instructor

Responsible Instructor	Responsibilities	Activity
Weber, Christoph , Dipl.Ing.(FH)		

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Study focus ET-Automation technology	4 - 4		5
Bachelor	Study focus KT-Communication technology	4 - 4		5

Contents

Comment

Brief introduction to MATLAB
Analog and discrete-time signals, sampling and aliasing,
properties of discrete-time systems(LTI)
Time domain analysis: Discrete convolution, difference equations, FIR and IIR-sytems
Frequency domain analysis: DFT and FFT algorithms, MATLAB implementation
Definition and properties of the Z-Transform, Z-domain view of causality, stability and frequency spectrum
Digital filter design: Properties of IIR - and FIR Filters,
Design techniques for FIR filter: window design, Parks-McClellan method
Design techniques for IIR filter: bilinear transformation , impulse invariant method
Several design examples are provided and implemented on a DSP evaluation board

Literature

von Grüningen, d. Ch. Digitale Signalverarbeitung
Fachbuchverlag Leipzig 2002
Werner, M. Digitale Signalverarbeitung mit MATLAB
Vieweg, Braunschweig 2003
Stearn, S. D. Digitale Verarbeitung analoger Signale
Oldenbourg Verlag, München 1991
Brigham, E. O. FFT - Schnelle Fourier-Transformation
Oldenbourg Verlag, München 1989
Götz, H. Einführung in die Digitale Signalverarbeitung
Teubner Verlag Stuttgart 1998
Kammeyer, K.-D., Kroschel, K. Digitale Signalverarbeitung
Teubner Verlag Stuttgart 1997
Hess, W. Digitale Filter
Teubner Verlag Stuttgart 1989

Remarks

This lecture based on the analog signal processing knowledge from other precided lectures.
Knowledge of digital signal pocessing in time and frequency domain. Exercising of Algorithm with MATLAB. Filter design and implementation on signal processors.

Access requirements

Fourier- and Laplace-Transformation

Exams accredited unmarked: --- .
marked: written examination, 90 minutes.

Structure Tree

This lecture was found in WiSe 2008/09 2 times:

University course catalog
- **Electrical Engineering & Information Te (Bachelor) ET**
  - **Stage II studies and study focus Automation** - - - 1
  - **Main field of study: Communication** - - - 2

Hint: you are in ***Winter 2008/09*** and not in the term which is in planning!

Object Oriented Programming - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-1805@lists.hs-weingarten.de		
Type of Course	Lecture/Practical training	**Long text**	
Number	1805	**Short text**	
Term	WS 2008/09	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 1 22.09.2008 - 05.10.2008 enrollment Hauptbelegungszeitraum 2 29.09.2008 - 12.10.2008 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Wed.	08:00 to 09:30	woch		Gebäude B - B 016						
	Thurs.	09:45 to 11:15	woch		Gebäude D - D002						

Functions:

Group [no name]: → application info

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Study focus ET-Automation technology	4 - 4		5
Bachelor	Applied Computer Science	2 - 2		5

Assign to Departments

Applied Computer Science

Contents

Comments

Course starts on Oct 8, 2008 in H002

Attention!

All participants for the laboratory have to register under the internet address "www.hs-weingarten.de/y_labore2" until October 12. Here students can specify their preferences for laboratory dates and group partners.

!! REGISTERING FOR THE LABORATORY IN THE "LSF" ALONE DOES NOT SUFFICE !!

Comment

- Fundamentals of object oriented programming
- Differences between C and C++ for conventional programming
- Classes, methods and inheritage
- Function overloading
- Constructors and destructors
- Virtual functions, polymorphism
- Exception handling
- Templates
- Operator overloading
- Standard Template Library

The lecture is accompanied by four programming exercises.

Literature

P.Prinz, U. Kirch-Prinz: C++ Lernen und professionell Anwenden
U. Breymann: C++ Eine Einführung

Remarks

The students will be able to implement simple software systems in the programming language C++

Access requirements

Programming language C

Exams accredited

unmarked: practical work.
marked: written examination, 90 minutes.

Information and Communication Engineering (Diploma)
Focus: Communication Technology
Focus: Automation Technology
unmarked: --- .
marked: written examination, 90 minutes.

Structure Tree

This lecture was found in WiSe 2008/09 2 times:

University course catalog
- Applied Computer Science (Bachelor) AI
  - Stage I studies - - - 1
- Electrical Engineering & Information Te (Bachelor) ET
  - Stage II studies and study focus Automation - - - 2

Hint: you are in ***Winter 2008/09*** and not in the term which is in planning!

Objektorientierte Programmierung Übungen - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructor | Required subject (SPO) | Departments | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-2323@lists.hs-weingarten.de	
Type of Course	Practical Exercise	**Long text**
Number	2323	**Short text**
Term	WS 2008/09	**Hours per week in term**
Expected no. of participants		**Max. participants**
Frequency	Every Term	**Study Year**
Hyperlink		
Language	German	
application periods	Hauptbelegungszeitraum 1 22.09.2008 - 05.10.2008 enrollment Hauptbelegungszeitraum 2 29.09.2008 - 12.10.2008 enrollment	

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Wed.	08:00 to 09:45	woch		Gebäude T - T 111						
	Tues.	11:45 to 13:15	woch		Gebäude T - T 013						

Functions:

Group [no name]: → application info

Responsible Instructor

Responsible Instructor	Responsibilities	Activity
Bernhard, Matthias , Dipl.Ing.(FH)	attendant	

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Study focus ET-Automation technology	4 - 4		
Bachelor	Applied Computer Science	2 - 2		

Assign to Departments

Applied Computer Science

Structure Tree

This lecture was found in WiSe 2008/09 2 times:

University course catalog
- → Applied Computer Science (Bachelor) AI
  - → Stage I studies - - - 1
- → Electrical Engineering & Information Te (Bachelor) ET
  - → Stage II studies and study focus Automation - - - 2

Hint: you are in ***Winter 2008/09*** and not in the term which is in planning!

Realtime programming - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructor | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-1494@lists.hs-weingarten.de		
Type of Course	Lecture	**Long text**	
Number	1494	**Short text**	
Term	WS 2008/09	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 1 22.09.2008 - 05.10.2008 enrollment Hauptbelegungszeitraum 2 29.09.2008 - 12.10.2008 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Tues.	16:00 to 17:30	Einzel	at 27.01.2009	Gebäude L - L129						
	Fr.	08:00 to 09:30	woch		Gebäude L - L129		Weissenbühler			23.01.2009: wird am 27.01.2009 von 16-17:30 Uhr nachgeholt Raum L129	

Functions:

Group [no name]: → application info

Responsible Instructor

Responsible Instructor	Responsibilities	Activity
Weissenbühler, Wolf-Dieter		

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Study focus ET-Automation technology	4 - 4		5
Bachelor	Study focus AI-Automation systems	5 - 5		5

Assign to Departments

Applied Computer Science

Contents

Comment

Bei der Programmierung von Steuerungs- und Überwachungssystemen für Geräte, Maschinen und Anlagen sowie für Embedded Systeme (z.B. elektronische Systeme von Fahrzeugen) sind neben den allgemein für die Erstellung von Programmen zu erfüllenden Anforderungen zusätzlich die Forderungen nach Rechtzeitigkeit , Gleichzeitigkeit und zeitlichem Determinismus zu erfüllen. Dies bedeutet, dass nicht nur die Fehlerfreiheit oder z.B. einfache Bedienbarkeit eines Programms von Bedeutung sind, sondern dass die Ergebnisse auch rechtzeitig zur Verfügung stehen müssen (Realzeitsysteme). Da bei solchen Anwendungen im Allgemeinen gleichzeitig mehrere Aufgaben zu erledigen sind (z.B. die zyklische Verarbeitung mehrerer Regelkreise, kontinuierliche Überwachung von Prozessgrößen oder Kommunikation mit anderen Systemen) sollte die gleichzeitige Verarbeitung mehrere Prozesse (Multitasking) möglich sein.
Im Rahmen der Lehrveranstaltung erhält der Studierende einen Einblick in die, in der Automatisierungstechnik heute üblichen Systemarchitekturen und Steuergeräte-Technologien. Der Schwerpunkt der Lehrveranstaltung befasst sich mit der Einführung in die Systematik der Echtzeitprogrammierung, d.h. in die, in der Automatisierungstechnik bzw. im Anwendungsbereich der Embedded Systeme heute übliche Programmiermethodik. Außerdem erfolgt eine Einführung in die Grundlagen der Echtzeitbetriebssysteme sowie die Grundlagen der Programmierung von speicherprogrammierbaren Steuerungen nach internationalem Standard.
Lehrinhalte:
Architektur moderner Automatisierungssysteme,
Zuverlässigkeit, Verfügbarkeit und Sicherheit von Systemen,
Spezifische Anforderungen an Echtzeitsysteme,
Möglichkeiten der Echtzeitverarbeitung: Zyklische Verarbeitung, zeitgesteuerte Verarbeitung, zyklische Verarbeitung mit Interrupts, Multitasking, Rate Monotonic Scheduling, Deadline Monotonic Analysis,
Leistungsmerkmale von Echtzeitbetriebssystemen: Preemptive und Non-Bei der Programmierung von Steuerungs- und Überwachung-s-systemen für Geräte, Maschinen und Anlagen sowie für Embedded Systeme (z.B. elektronische Systeme von Fahrzeugen) sind neben den allg-e-mein für die Erstellung von Programmen zu erfüllenden Anforderungen zusätzlich die Forderungen nach Rechtzeitigkeit , Gleichzeitigkeit und zeitlichem Determinismus zu erfüllen. Dies bedeutet, dass nicht nur die Fehlerfreiheit oder einfache Bedienbarkeit eines Programms von Bedeutung sind, sondern dass die Ergebnisse auch rechtzeitig zur Verfügung stehen müssen (Realzeitsysteme). Da bei solchen Anwendungen im Allgemeinen gleichzeitig mehrere Aufgaben zu erledigen sind (z.B. die zyklische Verarbeitung mehrerer Regelkreise, kontinuierliche Überwachung von Prozessgrößen oder Kommunikation mit anderen Systemen) sollte die quasi gleichzeitige Verarbeitung mehrerer Prozesse (Multitasking) möglich sein.
Im Rahmen der Lehrveranstaltung erhält der Studierende einen Einblick in die, in der Automatisierungstechnik heute üblichen Systemarchitekturen und Steuergeräte-Technologien. Der Schwerpunkt der Lehrveranstaltung liegt in der Einführung in die Methoden der Echtzeitprogrammierung, d.h. in die, in der Automatisierungstechnik bzw. im Anwendungsbereich der Embedded Systeme heute übliche Programmiermethodik. Außerdem erfolgt eine Einführung in die Grundlagen der Echtzeitbetriebssysteme sowie die Grundlagen der Programmierung von speicherprogrammierbaren Steuerungen.

Lehrinhalte
Architektur moderner Automatisierungssysteme,
Zuverlässigkeit, Verfügbarkeit und Sicherheit von Systemen,
Spezifische Anforderungen an Echtzeitsysteme,
Möglichkeiten der Echtzeitverarbeitung: Zyklische Verarbeitung, zeitgesteuerte Verarbeitung, zyklische Verarbeitung mit Interrupts, Multitasking,
Rate Monotonic Scheduling, Deadline Monotonic Analysis,
Leistungsmerkmale von Echtzeitbetriebssystemen: Preemptive und Non-Preemptive Scheduling,
Task- und Ressourcen Synchronisation (Semaphore Prinzip), Task Kommunikation (Message Passing-Prinzip), Grundlagen der Interruptverarbeitung, Zugriff auf gemeinsam genutzte Daten,
Entwurfskriterien für Echtzeitsysteme,
Vorstellung eines realen Echtzeitbetriebssystems,
Realisierungsbeispiel
Einführung in die Technologie der speicherprogrammierbaren Steuerungen, Programmierung speicherprogrammierbarer Steuerungen nach IEC 61131- 3.

Remarks Kenntnis der Architektur moderner Automatisierungssysteme. Grundverständnis der Programmierung von Echtzeitsystemen auf Basis von Echtzeitbetriebssystemen. Grundkenntnisse zur Programmierung von speicherprogrammierbaren Steuerungen.

Exams accredited unmarked: --- .
marked: written examination, 90 minutes.

Structure Tree

This lecture was found in WiSe 2008/09 3 times:

- University course catalog
  - Electrical Engineering & Information Te (Bachelor) ET
    - Stage II studies and study focus Automation - - - 1
  - Applied Computer Science (Bachelor) AI
    - Main field of study: Automation/Multimedia-Systems (Bachelor) - - - 2
    - Main field of study: Information Networks - - - 3

Course Overview
Course Overview (modularisiert)
Search for Lectures
Time table of study programs
Structure of Curricula modules-LSF
Edit Lecture
Lectures today
Lectures today (with Search)
Hide menu

Hint: you are in ***Winter 2008/09*** and not in the term which is in planning!

Realtime programming, Practical training - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-1495@lists.hs-weingarten.de		
Type of Course	Practical training	**Long text**	
Number	1495	**Short text**	
Term	WS 2008/09	**Hours per week in term**	2
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Study focus ET-Automation technology	4 - 4		2
Bachelor	Study focus AI-Automation systems	5 - 5		3

Assign to Departments

Applied Computer Science

Contents

Comment	Entwurf, Codierung (Programmiersprache C) und Test von kleinen Echtzeit-Programmsystemen zur Demonstration von Multitasking, Task-Synchronisation und Interruptverarbeitung auf Basis des Echtzeitbetriebssystems VxWorks. Bestimmung des Zeitbedarfs für Taskumschaltung. Ermittlung des Speicherbedarfs für die Einrichtung eines Task-Control-Blocks. Ermittlung von Laufzeit und Speicherbedarf einzelner Betriebssystem-Funktionen. Entwurf, Programmierung und Testen eines verteilten intelligenten Automatisierungssystems auf Basis von IEC 61131-3 und CANopen.
Remarks	Im Rahmen der Laborübungen erfolgt eine Vertiefung der in der Lehrveranstaltung Echtzeitprogrammierung vermittelten Grundlagen durch praktische Übungen.
Exams accredited	unmarked: practical work. marked: --- .

Structure Tree

Lecture not found in this Term. Lecture is in Term WS 2008/09 , Currentterm: Winter 2008/09

Hint: you are in ***Winter 2008/09*** and not in the term which is in planning!

BWL / Management - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Required subject (SPO) | Departments | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-4050@lists.hs-weingarten.de		
Type of Course	Lecture	**Long text**	
Number	4050	**Short text**	
Term	WS 2008/09	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Study focus KT-Communication technology	4 - 4		4
Bachelor	Study focus ET-Automation technology	4 - 4		4

Assign to Departments

Electrical Engineering and Information Technology

Structure Tree

Lecture not found in this Term. Lecture is in Term WS 2008/09 , Currentterm: Winter 2008/09

Hint: you are in ***Winter 2008/09*** and not in the term which is in planning!

Fertigungstechnologien für elektronische Baugruppen - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructor | Required subject (SPO) | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	lsf-veranstaltung-3300@lists.hs-weingarten.de		
Type of Course	Lecture	**Long text**	
Number	3300	**Short text**	
Term	WS 2008/09	**Hours per week in term**	2
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
application periods	Hauptbelegungszeitraum 1 22.09.2008 - 05.10.2008 enrollment Hauptbelegungszeitraum 2 29.09.2008 - 12.10.2008 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Tues.	08:00 to 09:30	woch		Gebäude H - H238		Röck				

Functions:

Group [no name]: → application info

Responsible Instructor

Responsible Instructor	Responsibilities	Activity
Röck, Markus , Dr.		

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Study focus KT-Communication technology	4 - 4		
Bachelor	Study focus ET-Automation technology	4 - 4		
Bachelor	Electrical Engineering & Information Technology	3 - 3		
Bachelor	Study Focus PT-Mechatronics/Optics	7 - 7		

Assign to Departments

Electrical Engineering and Information Technology

Contents

Comment This lecture is an introduction to materials and technologies for the manufacturing of electronic devices. The aim of the lecture is to give an understanding for the basic parameters of a high volume manufacturing.
This understanding is necessary for an electrical engineer to develop reliable and cost-efficient devices. Because 80% of the cost of an electronic device is defined during the development, this understanding is essential for a manufacturing in Europe.

Structure Tree

This lecture was found in WiSe 2008/09 2 times:

University course catalog
→ Electrical Engineering & Information Te (Bachelor) ET
→ Technische Wahlfächer - - - 1
→ Physical Engineering (Bachelor) PT
→ Technology electives - - - 2

Hint: you are in ***Winter 2009/10*** and not in the term which is in planning!

Software Engineering - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Required subject (SPO) | Exams / Modules | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-1810@lists.hs-weingarten.de		
Type of Course	Lecture	**Long text**	
Number	1810	**Short text**	
Term	WS 2009/10	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 1 21.09.2009 - 04.10.2009 enrollment Hauptbelegungszeitraum 2 28.09.2009 - 11.10.2009 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Wed.	14:15 to 15:45	woch		Gebäude T - T 117						
	Wed.	16:00 to 17:30	woch		Gebäude T - T 117						

Functions:

Group [no name]: → application info

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Study focus ET-Automation technology	6 - 6		5
Bachelor	Applied Computer Science	3 - 3		5

Exams / Modules

Number of Exam	Examination Version	Module
2234	10	Software-Engineering
3617	10	Software-Engineering

Assign to Departments

Applied Computer Science

Contents

Comment

Introduction;
Complexity of developments and systems;
Phases of program development and phase;
Documentation and communication in software development;
Resource evaluation;
Various means of documentation for analysis and draft (SA/SD, Jackson, SADT); Introduction to UML (Unified Modelling Language);
Object-orientated analysis;
Object-oriented draft principles;
Frameworks;
Draft of distributed systems (only basic principles);
Modular design of a system;
Special analysis and draft techniques;
Techniques for the coding phase;
Revision management;
Software quality assurance (basic principles);

Einführung; Komplexität von Entwicklungen und Systemen; Phasen der Programmentwicklung und Phasenmodelle; Dokumentation und Kommunikation in der Softwareentwicklung; Aufwandsschätzung; Verschiedene Dokumentationsmittel für Analyse und Entwurf (SA/SD, Jackson, SADT); UML; Definition und Konzeption; Objektorientierte Analyse; Objektorientierter Entwurf; Frameworks; Entwurf verteilter Systeme (nur Grundprinzipien);Aufteilung eines Systems in Komponenten (Modularisierung); Spezielle Analyse- und Entwurfsverfahren; Verfahren für die Codierphase; Revisionsführung; Software-Qualitätssicherung (Grundbegriffe)

Literature

Sommerville, I.; Software Engineering; 6. Aufl. Pearson Education Deutschland; ISBN 3-8273-7001-9.

Remarks

The students are supposed to get a fundamental introduction to the most important methods, procedures and tools of software engineering.
See: Practical training: Software Engineering

Probleme des Programmieren im Großen kennen und verstehen lernen. Kennen und Beherrschen der wesentliche Vorgehensweisen und Methoden des Softwareengineering. S.a. Praktikum Software-Engineering.

Access requirements

Lehrveranstaltungen der Semester 1 und 2, vor allem Programmieren, Objektorientierte Programmierung, Grundlagen der Informatik

Exams accredited	unmarked: --- . marked: written examination, 90 minutes.

Structure Tree

This lecture was found in WiSe 2009/10 2 times:

University course catalog
- **Applied Computer Science (Bachelor) AI**
  - **Stage I studies** - - - 1
- **Electrical Engineering & Information Te (Bachelor) ET**
  - **Stage II studies and study focus Automation** - - - 2

Hint: you are in ***Winter 2009/10*** and not in the term which is in planning!

Control Engineering - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Contents | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-2155@lists.hs-weingarten.de		
Type of Course	Lecture/Exercise	**Long text**	
Number	2155	**Short text**	
Term	WS 2009/10	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		

Contents

Comment

Basics
Mathematical Determination of control systems
- in time-, s- and frequency-domain
- Elementary and standard-models
Simple linear closed loop
-Components, requirements, stability, stationary and transient behavior
Closed loop design
- design using BODE-Plot

Literature

Unbehauen, Heinz: Regelungstechnik Bd. 1, Vieweg Braunschweig
Leonhard, Werner: Einführung in die Regelungstechnik, Vieweg, Braunschweig
Mann/Schiffelgen/Froriep: Einführung in die Regelungstechnik (MATLAB-Beispiele), Carl Hanser, München
Lutz/Wendt: Taschenbuch der Regelungstechnik, Harri Deutsch, Frankfurt/M.
Föllinger, Otto: Regelungstechnik Elitera, Berlin
Leonhard, / Schnieder: Aufgabensammlung zur Regelungstechnik Vieweg, Braunschweig
Pestel / Kollmann: Grundlagen der Regelungstechnik Vieweg, Braunschweig (mit Übungsaufgaben)
Mann / Schiffelgen / Froriep: Einführung in die Regelungstechnik (mit MatLab-Beispielen) Carl Hanser, München
Dörrscheidt / Latzel: Grundlagen der Regelungstechnik Teubner, Stuttgart
Lutz / Wendt Taschenbuch der Regelungstechnik Harri Deutsch, Frankfurt /M.
Glattfelder / Schaufelberger Lineare Regelsysteme, Eine Einführung mit MATLAB, Hochschulverlag ETH Zürich
Bode, Helmut MATLAB in der Regelungstechnik Teubner, Stuttgart
Walter, Hildebrand Kompaktkurs Regelungstechnik Vieweg, Braunschweig

Remarks

Besides measurement and control technologies, control engineering is an important pillar of the automation technique. The fundamental equipment is the theory of the system and reflection on linear transmission links appearing in control engineering. Before proceeding on to the actual controller design, an exact knowledge of the process (plant) is essential. For this reason a mathematical model of the plant is developed in either the experimental or the theoretical way. Based on this model, the controller design is carried out presenting various processes. The closed loop is investigated for its stationary and dynamic reaction considering its stability reaction in particular.

Access requirements

Differentialgleichungen, Laplace-Transformation

Exams accredited

unmarked: practical work.
marked: written examination, 90 minutes.

Physical Engineering (Diploma)
Focus: Substaniable Process Technology
Focus: Mechatronics
unmarked: --- .
marked: written examination, 90 minutes.

Information and Communication Engineering (Diploma)
Focus: Mechatronics
Focus: Automation Technology
unmarked: --- .
marked: written examination, 90 minutes.

Structure Tree

Lecture not found in this Term. Lecture is in Term WS 2009/10 , Currentterm: Winter 2009/10

Hint: you are in ***Winter 2009/10*** and not in the term which is in planning!

Control Engineering, Practical Training - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Required subject (SPO) | Exams / Modules | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	ⓘ: lsf-veranstaltung-3485@lists.hs-weingarten.de		
Type of Course	Practical training	**Long text**	
Number	3485	**Short text**	
Term	WS 2009/10	**Hours per week in term**	2
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 1 21.09.2009 - 04.10.2009 enrollment Hauptbelegungszeitraum 2 28.09.2009 - 11.10.2009 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Mo.	08:00 to 09:30	woch		Gebäude T - T 013						

Functions:

Group [no name]: → application info

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering/Applied Physics PLUS	7 - 7		2
Bachelor	Study focus AI-Automation systems	5 - 5		2

Exams / Modules

Number of Exam	Examination Version	Module
3637	10	Regelungstechnik Prakt.

Assign to Departments

Applied Computer Science

Contents

Exams accredited unmarked: practical work

Structure Tree

This lecture was found in WiSe 2009/10 2 times:

University course catalog
- → Applied Computer Science (Bachelor) AI
  - → Main field of study: Automation/Multimedia-Systems (Bachelor) - - - 1
  - → Main field of study: Information Networks - - - 2

Hint: you are in ***Winter 2009/10*** and not in the term which is in planning!

Industrial Communication Systems - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructor | Required subject (SPO) | Exams / Modules | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	lsf-veranstaltung-1906@lists.hs-weingarten.de		
Type of Course	Lecture	**Long text**	
Number	1906	**Short text**	
Term	WS 2009/10	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 1 21.09.2009 - 04.10.2009 enrollment Hauptbelegungszeitraum 2 28.09.2009 - 11.10.2009 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Wed.	09:45 to 11:15	woch		Gebäude L - L028		Schulter				
	Thurs.	11:30 to 13:00	woch		Gebäude K - K 103						

Functions:

Group [no name]: → application info

Responsible Instructor

Responsible Instructor	Responsibilities	Activity
Schulter, Wolfgang, Professor		

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Study focus ET-Automation technology	6 - 6	mandatory subject	5

Exams / Modules

Number of Exam	Examination Version	Module
2237	10	Industr.Kommunikationssy.

Assign to Departments

Electrical Engineering and Information Technology

Contents

Comment	--
Remarks	--
Exams accredited	unmarked: --- . marked: written examination, 90 minutes.

Structure Tree

This lecture was found in WiSe 2009/10 1 times:

University course catalog
→ Electrical Engineering & Information Te (Bachelor) ET
→ Stage II studies and study focus Automation - - - 1

Hint: you are in ***Winter 2009/10*** and not in the term which is in planning!

Microcontrollers - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructor | Required subject (SPO) | Exams / Modules | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	lsf-veranstaltung-2143@lists.hs-weingarten.de		
Type of Course	Lecture	**Long text**	
Number	2143	**Short text**	
Term	WS 2009/10	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	30
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 1 21.09.2009 - 04.10.2009 enrollment Hauptbelegungszeitraum 2 28.09.2009 - 11.10.2009 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Tues.	14:15 to 15:45	woch		Gebäude H - H 216						
	Tues.	14:15 to 15:45	woch		Gebäude H - H239		Brümmer			27.10.2009: Verschiebung durch Hochschulratssitzung	
	Tues.	16:00 to 17:30	woch		Gebäude H - H239					27.10.2009: Verschiebung durch Hochschulratssitzung	
	Tues.	16:00 to 17:30	woch		Gebäude H - H 216						

Functions:

Group [no name]: → application info

Responsible Instructor

Responsible Instructor	Responsibilities	Activity
Brümmer, Franz, Professor, Dr.-Ing.		

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering/Applied Physics PLUS	4 - 4	mandatory subject	4
Master mit vorausg. Absch	Computer Science	1 - 3	elective mandatory subject	5
Master mit vorausg. Absch	Optical Systems Engineering	2 - 2		5
Bachelor	Study focus ET-Automation technology	7 - 7	mandatory subject	5

Exams / Modules

Number of Exam	Examination Version	Module
2238	10	Microcontroller

Assign to Departments

Electrical Engineering and Information Technology

Contents

Comment

Short introduction to the developing process of processors with a focus on microcontrollers. Explanation of the architectural features of 8 bit controllers on the base of the 8051 family. A typical application with a member of these family, the 80535 will shown the all the aspects of building and programming such a controller.

The special features of 16 bit controllers will be explained on the MC68HC12 controller. The programming of these controller will be done mostly in C. But an introduction to the the opportunities to build interfaces between C- and assembler programs will also be given.

The programming of both controller families would be done parallel to the lecture on existing controller kits.

Literature

Kenneth J. Ayala: "The 8051 Microcontroller", West Publishing Company, 1997
Michael Kheir: "The MC68HC11 Microcontroller Application in Control, Instrumentation and Communication", Prentice Hall, 1996
Gordon Doughman: " Programming the Motorola M68HC12 Family", Annabooks, 2000

"SDCC Small Device C-Compiler", http://sdcc.sourceforge.net/snap.php
"GNU Development Chain for 68HC11 & 68HC12", http://savannah.gnu.org.projects/m68hc11

Remarks
Introduction to typical 8- and 16-bit microcontrollers with an explanation of architectural functions and special features in relation to microprocessors. More details of 8 bit controllers would be explained on the 8051 family and special functions of 16 bit controllers on the MC68HC12 family. Introduction to the limitations of fixpoint-arithmetic. Realisation of typical controller applications with programming steps in assembly language and C.

Access requirements
Knowledges in digital circuits to understand how processors are working and how to build boards with microcontrollers.
Additional knowledges about the architecture of processors and how to program processors in assembler are helpful.

Exams accredited
unmarked: --- .
marked: written examination, 90 minutes.
See also "Offizielle Aushänge".

Information and Communication Engineering (Diploma)
Focus: Automation Technology
unmarked: --- .
marked: written examination, 120 minutes.
(Hint: will be examined with
Mikrocontroller-Technik
Computer Architecture)

Structure Tree

This lecture was found in WiSe 2009/10 4 times:

University course catalog
- Electrical Engineering & Information Te (Bachelor) ET
  - Stage II studies and study focus Automation - - - 1
- Elektrotechnik/Physik PLUS - - - 2
- Computer Science (Master) - - - 3
- Optical Systems Engineering (Master) OS - - - 4

Hint: you are in ***Winter 2009/10*** and not in the term which is in planning!

SPS Systems - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructor | Required subject (SPO) | Exams / Modules | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	lsf-veranstaltung-1922@lists.hs-weingarten.de		
Type of Course	Lecture	**Long text**	
Number	1922	**Short text**	
Term	WS 2009/10	**Hours per week in term**	2
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 1 21.09.2009 - 04.10.2009 enrollment Hauptbelegungszeitraum 2 28.09.2009 - 11.10.2009 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Thurs.	08:00 to 09:30	woch		Gebäude L - L129		Altmann				
	Thurs.	09:45 to 11:15	woch		Gebäude L - L129						

Functions:

Group [no name]: → application info

Responsible Instructor

Responsible Instructor	Responsibilities	Activity
Altmann, Bernd, Professor, Dr.-Ing.		

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering/Applied Physics PLUS	7 - 7	mandatory subject	2
Bachelor	Study focus ET-Automation technology	6 - 6	mandatory subject	3

Exams / Modules

Number of Exam	Examination Version	Module
2239	10	SPS-Systeme

Assign to Departments

Electrical Engineering and Information Technology

Contents

Comment	Introduction to PLC (hardware, software, functioning). Presentation of EASY 800 system features and programming. Presentation of XC 100 system features and programming. Presentation of various simulated models of technical processes (pneumatic cylinder, punching, lifting and sorting, drilling, rotating table, modular production system, robot handling system, etc.) Introduction to describing methods of the dynamic sequential behaviour of technical processes The practical part is done by the students themselves with hands on equipment: Describe the dynamic behaviour the simulated technical process Develop and test the SW (ladder diagram) for EASY 822 DC TC Connect EASY to simulated process, download SW to EASY, perform commissioning and test of system Repeat above mentioned steps with different process models of increasing complexity Develop and test the SW (IL, SFC, etc.) for PLC XC 100 Connect XC100 to simulated process, download SW to XC100, perform commissioning and test of system Repeat above mentioned steps with a selected model of more complexity Connect the PLC XC 100 to a hardware process, develop XC100 SW and perform commissioning and testing
Remarks	Understand the functioning of PLC controller; be able to describe the dynamic behaviour of technical processes, learn programming of a plc according to IEC 1131, and perform commissioning and testing of an automated technical process.
Access requirements	Digitaltechnik, Elektrotechnik, Elektronik
Exams accredited	unmarked: --- . marked: written examination, 90 minutes.

Structure Tree

This lecture was found in WiSe 2009/10 1 times:

University course catalog
→ Electrical Engineering & Information Te (Bachelor) ET
→ Stage II studies and study focus Automation - - - 1

Hint: you are in ***Winter 2009/10*** and not in the term which is in planning!

SPS Systems, Practical Training - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructor | Required subject (SPO) | Exams / Modules | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	lsf-veranstaltung-1923@lists.hs-weingarten.de		
Type of Course	Practical training	**Long text**	
Number	1923	**Short text**	
Term	WS 2009/10	**Hours per week in term**	2
Expected no. of participants		**Max. participants**	20
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 1 21.09.2009 - 04.10.2009 enrollment Hauptbelegungszeitraum 2 28.09.2009 - 11.10.2009 enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Mo.	14:15 to 15:45	woch		Gebäude L - L118		Altmann				
	Mo.	16:00 to 17:30	woch		Gebäude L - L118						

Functions:

Group [no name]: → application info

Responsible Instructor

Responsible Instructor	Responsibilities	Activity
Altmann, Bernd, Professor, Dr.-Ing.		

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering/Applied Physics PLUS	7 - 7	mandatory subject	2
Bachelor	Study focus ET-Automation technology	6 - 6	mandatory subject	2

Exams / Modules

Number of Exam	Examination Version	Module
2246	10	SPS-Systeme Praktikum

Assign to Departments

Electrical Engineering and Information Technology

Contents

Comments	Das SPS-Systeme Praktikum beginnt etwa in Semestermitte und wird in der zughörigen Vorlesung angekündigt.
Comment	--
Remarks	--
Access requirements	Vorlesung SPS Systeme parallel hören, sowie dortige Voraussetzungen
Exams accredited	unmarked: practical work. marked: --- . See also "Offizielle Aushänge"

Structure Tree

This lecture was found in WiSe 2009/10 1 times:

University course catalog
→ Electrical Engineering & Information Te (Bachelor) ET
→ Stage II studies and study focus Automation - - - 1

Hint: you are in ***Summer 2011*** and not in the term which is in planning!

Electro-Motive Engineering - Single View

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Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Responsible Instructor | Required subject (SPO) | Exams / Modules | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	lsf-veranstaltung-48@lists.hs-weingarten.de		
Type of Course	Lecture/Exercise	**Long text**	
Number	48	**Short text**	
Term	SS 2011	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application periods	Hauptbelegungszeitraum 1 28.02.2011 - 13.03.2011 **enrollment** Hauptbelegungszeitraum 2 07.03.2011 - 20.03.2011 **enrollment**		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	Fr.	13:30 to 15:00	Einzel	at 13.05.2011	Gebäude N - N042						
	Mo.	11:30 to 13:00	woch		Gebäude H - H061 (max. 30 Personen)		Kastner				
	Fr.	14:15 to 15:45	woch		Gebäude H - H061 (max. 30 Personen)						

Functions:

Group [no name]: → application info

Responsible Instructor

Responsible Instructor	Responsibilities	Activity
Kastner, Günther, Professor, Dr.-Ing.		

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Electrical Engineering/Applied Physics PLUS	4 - 4		5
Bachelor	Automotive Engineering PLUS	6 - 6		5
Bachelor	Study Focus PT-Energy/Process Engineering	4 - 6		5
Bachelor	Study focus ET-Automation technology	6 - 7		5

Exams / Modules

Number of Exam	Examination Version	Module
2247	10	Elektrische Antriebe
5128	10	Elektr. Antriebstechnik
5128	11	Elektr. Antriebstechnik
5428	10	Elektr. Antriebstechnik
5428	11	Elektr. Antriebstechnik
5635	10	Elektr. Antriebstechnik
5635	11	Elektr. Antriebstechnik
7636	10	Einf. Antriebstechnik
7636	11	Einf. Antriebstechnik

Assign to Departments

Mechanical Engineering

Contents

Comment

Contents:
Motor and load characteristics
Duty classes
Application engineering of drives

	Servo motors Power electronics: components and circuits Control of drives
Literature	Brosch: Moderne Stromrichterantriebe
Remarks	Objectives: The course is designed to teach the fundamentals of electric drive systems. Dc motors, asynchronous motors and servo motors: behaviour and application.
Access requirements	Electrical engineering
Exams accredited	unmarked: --- . marked: written examination, 90 minutes.

Structure Tree

This lecture was found in SoSe 2011 9 times:

- **University course catalog**
  - **Automotive Engineering PLUS (Bachelor) FP**
    - **Stage II studies** --- 1
  - **Electrical Engineering & Information Te (Bachelor) ET**
    - **Stage II studies and study focus Automation** --- 2
  - **Mechanical Engineering (Bachelor) MB**
    - **Stage II studies and main field ofstudy General Mechanical Engineering** --- 3
    - **Stage II studies and main field of study Energy and Process Technology** --- 4
    - **Stage II studies and main field of study Production Technology** --- 5
  - **Automotive Engineering (Bachelor) FT**
    - **Main field of study : Vehicle technology** --- 6
    - **Stage II studies and study focus Mechatronics for Vehicle systems** --- 7
  - **Elektrotechnik/Physik PLUS** --- 8
  - **Physical Engineering (Bachelor) PT**
    - **Technology electives** --- 9

Hint: you are in *Summer 2011* and not in the term which is in planning!

Presentation/Documentation - Single View

Go Back

Functions: Handle applications

Page contents: Basic Information | Dates/Times/Location | Required subject (SPO) | Exams / Modules | Departments | Contents | Structure Tree

Basic Information

E-Mailadresse	lsf-veranstaltung-3679@lists.hs-weingarten.de		
Type of Course	Block seminar	**Long text**	
Number	3679	**Short text**	
Term	SS 2011	**Hours per week in term**	4
Expected no. of participants		**Max. participants**	30
Frequency	Every Term	**Study Year**	
Hyperlink			
Language	German		
application period	Belegungszeitraum Präsentation/Dokumentation 15.02.2011 - 05.03.2011		
	enrollment		

Dates/Times/Location Group: [no name]

	Day	Time	Frequency	Duration	Room	Room-plan	Lecturer	Status	Remarks	Cancelled on	Max. participants
	-.	08:00 to 17:30	Block	07.03.2011 to 12.03.2011	Gebäude L - L028						

Functions:

Group [no name]: → application info

Required subject (SPO)

Graduation	Curricula	Term	Kategorie	ECTS
Bachelor	Study focus ET-Automation technology	7 - 7	mandatory subject	6
Bachelor	Study focus KT-Communication technology	7 - 7	mandatory subject	6

Exams / Modules

Number of Exam	Examination Version	Module
2250	10	Präsentation/Dokument.

Assign to Departments

Electrical Engineering and Information Technology

Contents

Comment

many exercises
preparation methods
idea development methods
communication model
presentation structure
body language (eye contact,)
media design (design rules and design variables)
advantages and disadvantages of media
different kinds of documentations (paperworks,)

Literature

Put it into practice!

Remarks

In this lecture, you will learn how to prepare and give effective presentations and documentations.

After this class you will be able to:
reach your presentation s aim successfully
communicate confidently with your audience
use all kinds of media (images, sound, video, animation) convincingly, according to your criteria
give good presentations
reduce self-conscience and gain self-confidence
prepare, give and improve presentations efficiently through the use of check lists
prepare your documentation (paperwork,) individually, aiming for your respective target group

Access requirements

None!

Exams accredited

marked: oral examination

Structure Tree

This lecture was found in SoSe 2011 2 times:

University course catalog
→ Electrical Engineering & Information Te (Bachelor) ET
→ Stage II studies and study focus Automation - - - 1
→ Main field of study: Communication - - - 2